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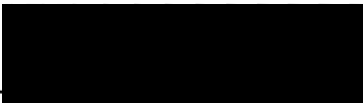
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THESIS APPROVAL

The abstract and thesis of Swarupa Madhav Nerurkar for the
M.S. degree in Electrical Engineering
were presented November 29, 1993 and accepted by the thesis
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ABSTRACT

An abstract of the thesis of Swarupa Madhav Nerurkar for the Masters of Science in Electrical Engineering presented November 29, 1993

Title: Modeling and Simulation of Bipolar Transistor at Low Temperature

The BICMOS technology which integrates the CMOS technology with bipolar technology has drawn considerable attention as an attractive VLSI technology because of the high speed performance and low power consumption of the BICMOS. However, continued down scaling of CMOS devices has caused increased concerns with problems such as latch up, hot carriers and short channel effect. Most of the above mentioned problems can be avoided by operating the CMOS at liquid-nitrogen temperature(LNT). At low-temperatures, the CMOS exhibits lower sub threshold leakage, higher carrier mobility (which yields improved speed performance), and a steeper logarithmic current-voltage slope. On the other hand, the low-temperature operation of conventional silicon bipolar circuits has been generally dismissed as impractical because of the well known decrease in the current gain at low temperature. The present interest in integrated bipolar-CMOS circuits, plus the prospect of increased reliability, lower wiring delay, and lower noise, has revised interest in low-temperature bipolar devices. In this context, it is therefore important to acquire accurate knowledge of the transistor properties at liquid nitrogen temperature. This can be done in two ways. One is through experimental low-temperature measurements and the other by low-temperature device simulations.

Existing room temperature numerical simulators are typically not useful for low temperature conditions. This is because the physical assumptions such as complete ionization, the parameter models and implementation methods for room temperature condition do not hold at low temperature. Therefore, we used BiLow - a steady state one-dimensional Bipolar Low Temperature Simulator for the temperature range of 77K-300K. This simulator, originally written in FORTRAN, was converted to C for the dual purpose of proper memory management and making further modifications easier. The focus of this research has been to model bandgap narrowing, incomplete ionization and Mott Transition at room and at low-temperature, evaluate the performance of the new BiLow and to derive conclusions on the BJT performance at LNT.

It was observed that the bandgap narrowing was independent of temperature for the entire range of majority carrier concentration. The effect of Mott transition on the abrupt decrease in the electron concentration in emitter has been taken care of by smoothing out the concentration profile in the emitter thereby providing a continuity in the region of Mott transition. Both the current gain(β) and the frequency(f_T) values obtained from simulating the two new profiles were found to be smaller than those obtained using the original BiLow simulator, as the doping in the base is higher and the device sizes were smaller. Most of the degradation in β and f_T was found to occur below 150K. From the plots of the charge characteristics, we found that the total charge which is a strong function of temperature is more in the case of the profiles studied for this work than the total charge from the original BiLow simulator.

MODELING AND SIMULATION OF BIPOLAR TRANSISTOR
AT
LOW TEMPERATURE

by

SWARUPA MADHAV NERURKAR

A thesis submitted in partial fulfillment of the
requirements for the degree of

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CHAPTER I

INTRODUCTION

Novel fabrication methods have made a growing variety of electronic devices considerably more complex than earlier designs. Over the years, device size and critical dimensions have shrunk by orders of magnitude. Impurity doping densities have increased greatly and different spatial variation of impurity profile has become feasible. Yet, despite these advancements in semiconductor technology, physical understanding in the realm of small devices, highly doped devices, or devices with graded composition remains incomplete. The BICMOS technology which integrates the CMOS technology with bipolar technology has drawn considerable attention as an attractive VLSI technology because of the high speed performance and low power consumption [43] of the BICMOS. However, continued down scaling of CMOS devices has caused increased concerns with problems such as latch up, hot carriers and short channel effect. Most of the above mentioned problems can be avoided by operating the CMOS at liquid-nitrogen temperature(LNT). At low-temperatures, the CMOS exhibits lower sub threshold leakage, higher carrier mobility (which yields improved speed performance), and a steeper logarithmic current-voltage slope. On the other hand, the low-temperature operation of conventional silicon bipolar circuits has been generally dismissed as impractical because of the well known decrease in the current gain at low temperature [13]. The present interest in integrated bipolar-CMOS circuits, plus the prospect of increased reliability, lower wiring delay, and lower noise, has revised interest in low-temperature bipolar devices. In this context, it is therefore important to acquire accurate

knowledge of the transistor properties at liquid nitrogen temperature. This can be done in two ways. One is through experimental low-temperature measurements and the other by low-temperature device simulations. The accuracy of simulation largely depends on device modeling. Device modeling based on self-consistent solution of fundamental semiconductor equations dates back to the work of Gummel in 1964 [18]. However, the first application of this style of modeling for problems at low temperature was first carried out by Gaensslen et al in 1976 [17]. The main reason for this delay cannot only be seen in the lesser demands for low temperature simulation. The primary reason for the fairly poor status in fully numerical low-temperature device simulation stems from considerably increased difficulties regarding physical assumptions and implementation of the numerical solution [45].

Existing room temperature numerical simulators are typically not useful for low temperature conditions. This is because the physical assumptions such as complete ionization, the parameter models and implementation methods for room temperature condition do not hold at low temperature. Therefore, we used BiLow [5]- a steady state one-dimensional Bipolar Low Temperature Simulator for the temperature range of 77K - 300K. This simulator, originally written in FORTRAN, was converted to C for the dual purpose of proper memory management and making further modifications easier. In the process, iterative loops were simplified to make experimenting with different models simpler. The availability of dynamic memory allocation facility of the C-language can be used to easily extend it so that large problems can be accommodated without the smaller problems having to pay the penalty of huge unused memory. Widespread availability of C on different platforms (including PCs) and extensive C literacy of EE students should make continued refinement of this program possible. Besides, the need to evaluate and update the models used in the simulator is always necessary.

Transistors have changed a great deal in the last ten years. The work of Schlig [44] and the interpretation by Dumke [13] were based on diffused transistors, whereas the self-aligned transistors still predominate [38]. The advent of self-aligned process, and the incorporation of the polysilicon emitter contact, results in transistors with higher current gain [38]. This makes scaling of the vertical dimensions possible, leading to smaller and faster transistors. This in turn demands greater base doping. At low-temperature, these changes affect among other things, two of the most important figures of merit predicting circuit performance, namely current gain (β) and cutoff frequency (f_T). The difference in the apparent bandgap narrowing (BGN) between the base and the emitter of a BJT is a very important factor in β degradation at low temperature. Most of the discrepancies in the BGN values arise from the differences in the assumptions made about the minority- and majority-carrier mobility's and in some cases, from the lifetime values used in the interpretation of the experimental data. Because of the significance in a wide variety of application, considerable effort has been expended to obtain empirical information on the magnitude of the bandgap narrowing effect. A comparison between theoretical [23] and experimental [9]-[10], [26] BGN data shows a difference of upto 60% between them.

The lack of comparison of simulated to experimental data of some device parameter models necessitates the evaluation of the existing models and their relative impact on device simulations over temperatures. In this work, we have tried to compare the available literature data on bandgap narrowing so as to come up with a model of BGN for a wide range of doping concentrations. We have presented the literature survey in two groups. Group1 talks about the optical and experimental work on BGN and Group 2 deals with the theoretical works of various authors. After evaluating a lot of models we have chosen two BGN models, one as described by del Alamo [58] and the other described by Kuzmich [27]. Both the models are described by an analytical expression which can be

easily implemented in the simulator. Although, Fermi-Dirac statistic has been used in the original version of BiLow, the bandgap narrowing parameters were evaluated using Boltzman statistics. Therefore, we have modified the BGN model used in the original version by adding a correction to account for Fermi-Dirac statistics. In the BGN models used, we have assumed that the majority carrier concentration is equal to the ionized impurity concentration. Therefore, it is important to accurately model the ionized impurities as the BGN model is influenced by them.

Another important phenomena that needs careful attention during low-temperature simulations is that of incomplete ionization. For most room temperature applications complete ionization of impurities is assumed. However, at low-temperatures only a small fraction of the impurity atoms are ionized. In addition, at high doping concentration all impurity atoms get ionized independently of temperature and the semiconductor starts acting like a metal. This phenomenon is known as Mott transition. This necessitates the proper modeling of these two phenomena. In order to incorporate the incomplete ionization and Mott transition into the realm of device simulations, we have studied the incomplete ionization model proposed by Wieslaw Kuzmich [25]. We evaluated this model at room and at low-temperature and then included it into the simulator. It is also very important for the bandgap narrowing calculations to include the concentration of ionized impurities, as the BGN models assumed that the majority carrier concentration is equal to the ionized impurity concentration. We have used the incomplete ionization and Mott transition data to extract the BGN values as the bandgap narrowing models use majority-carrier concentration in the calculation of the BGN parameters.

The focus of this research has been to model bandgap narrowing, incomplete ionization and Mott Transition at room and at low-temperature, evaluate the performance of the new BiLow and to derive conclusions on the BJT performance at LNT. Chapter II talks about the existing models, their drawbacks and the basic assumptions used in the

simulator BiLow. The incomplete ionization model including the Mott transition model has been described and the results presented in Chapter III. The performance of two models for bandgap narrowing is compared along with the existing model in the simulator in Chapter IV. The results from the new simulator are presented in the final Chapter V.

CHAPTER II

LOW TEMPERATURE SIMULATION

I.1 Basic Assumptions

The performance of the bipolar transistor at low temperature depends on the modeling of the physical parameters of Poisson's equation and the electron and hole transport equations. In this section we give a brief description of the basic assumptions made, and the parameter models used by the original BiLow [5]. A one-dimensional simulator, was chosen since it can successfully simulate much of the overall bipolar transistor behavior.

In order to describe the performance of semiconductor devices at low temperature, the electron and hole concentrations, and the electrostatic potential, must be calculated using these semiconductor equations of state [45]:

$$\frac{d^2 \psi}{dx^2} - \frac{q}{E(n - p + N_A^- - N_D^+)} = 0 \quad (1)$$

$$\frac{1}{q} \frac{dJ_n}{dx} - R_{net} = 0 \quad (2)$$

$$\frac{1}{q} \frac{dJ_p}{dx} - R_{net} = 0 \quad (3)$$

$$J_n = -qD_n \frac{dn}{dx} - q\mu_n nE \quad (4)$$

$$J_p = qD_p \frac{dp}{dx} + q\mu_p pE \quad (5)$$

Where eqn.(1) is Poisson's equation, and eqn.(2) and (3) are the continuity equations and (4) and (5) are the current densities for electrons and holes respectively. In the above equations, n and p are the electron and hole carrier concentrations, ψ is the electrostatic voltage potential, N_D^+ and N_A^- are the ionized donor and acceptor impurity dopant concentrations, J_n and J_p are the electron and hole current densities, R_{net} represents the net recombination rate, x is the position coordinate μ is the mobility, E is the electric field and D_n and D_p represent the diffusion length of electrons and holes respectively. For numerical implementation, eqns. (1) - (3) are normalized and discretized on a one-dimensional non-uniform mesh. Due to normalization, eqns. (1) - (3) not only result in simpler, unitless equations, but it also decreases the numerical error that might otherwise occur. The equations are discretized using finite difference technique.

The one dimensional structure used by BiLow [5] is shown in Fig 1.

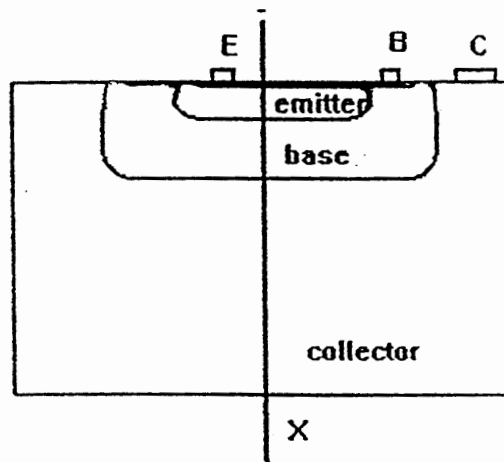


Figure 1. One Dimensional Bipolar Transistor Device Model

The definition of the mesh used in the original BiLow is illustrated in Fig 2. The mesh points defined as auxillary midpoints such as point K are located midway between nodes N and N+1. Because of the nature of the problem, the nodes are spaced non uniformly, based on the knowledge of the dependent variable distribution along the structure, which must be provided by the user.

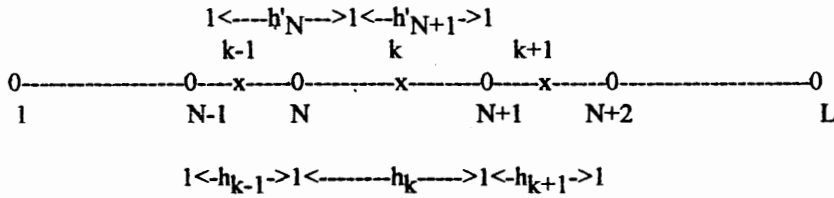


Figure 2. Mesh Definition

After normalization and discretization, eqns.(1), (2) and (3) are transformed into the following set of difference equations [45] :

$$\frac{h_{k-1} \psi_{N+1} - 2h'_N \psi_N + h_k \psi_{N-1}}{h_k h_{k-1} h'_N} = n_N - p_N + N_{AN^-} - N_{DN^+} \quad (6)$$

$$\frac{J_{nk} - J_{nk-1}}{h'_N} = +R_{net} N \quad (7)$$

$$\frac{J_{pk} - J_{pk-1}}{h'_N} = -R_{net} N \quad (8)$$

where the subscripts indicate at which nodes or auxillary midpoints the quantities in the equations are evaluated. For example, n_N represents the normalized electron concentration at node N.

Following the work of Gummel [3], the electron and hole current density expressions were represented in an integral form to increase stability. This yields the following pair of discretized equations:

$$J_{nk} = \frac{q}{h_k} D_{nk} [B(V_{nk})n_N - B(-V_{nk})n_{N+1}] \quad (9)$$

$$J_{pk} = \frac{q}{h_k} D_{pk} [B(-V_{pk})p_N - B(V_{pk})p_{N+1}] \quad (10)$$

where

$$V_{nk} = V_{pk} = \frac{q}{kT} \{ \psi(N) \psi(N+1)^{\frac{1}{2}} [T(N) - T(N+1)] \} \quad (11)$$

$$\text{and } B(Y) = \frac{Y}{e^Y - 1} \quad (12)$$

is called the Bernulli function. The numerical implementation of the Bernulli function is taken from Selberherr [45]. In order to obtain eqns. (9) and (10), the electric field, current density and mobility were assumed to be constant in the range of x between mesh points N and $N+1$.

After discretization on a one-dimensional non uniform mesh, the system of equations can be represented as:

$$F_{\psi}(\psi, n, p) = 0 \quad (13)$$

$$F_n(\psi, n, p) = 0 \quad (14)$$

$$F_p(\psi, n, p) = 0 \quad (15)$$

Each of the differential equations are treated separately by decoupling the system and solving eqns. (13)-(15) successfully. The Poisson eqn. (13) is solved assuming known electron and hole concentration, n and p . Then comes the solution for each of the continuity equations with ψ given from the first step. To solve each of the non-linear equations from eqns.(13)-(15), an iterative method is chosen due to its quadratic

convergence. A substantial reduction in CPU time can be achieved by this method. Applying this method to Poisson's equation, the system (13) is linearized and the iteration process is defined as :

$$\frac{dF_{\psi}}{d\psi} d\psi_{k+1} = -F_{\psi}(x_k) \quad \psi_{k+1} = \psi_k + d\psi_{k+1}; \quad k = 0, 1, 2, \dots \quad (16)$$

$\frac{dF_{\psi}}{d\psi}$ is the Jacobian matrix evaluated at ψ_k and $d\psi_{k+1}$ is the correction vector of iteration step $k+1$. This simple implementation of the Newton's method results in slow convergence if eqns. (13)-(15) are strongly coupled. The coupled and decoupled methods can be combined into a hybrid solution method [45], which starts with the decoupled method and switches to the coupled one if the solution is not reached after a certain number of iterations. The hybrid approach has been successfully used in BiLow, and the flowchart for the operation of the simulator is given in Fig 3. The carrier and potential distribution for one bias point is used as initial condition for a new bias point for the coupled solver. If these values are not available, the simulator starts using the decoupled solver with zero bias condition and then increments the bias voltage in small steps using the coupled solver until the desired bias conditions are achieved. The relative errors $\frac{dp}{p}$,

$\frac{dn}{n}$, and $\frac{d\psi}{\psi}$ are used as basic convergence criteria. The relative errors between two successive Newton iterations has to be less than 10^{-4} . The accuracy of the simulation is checked by calculating the total current density, which should be constant through the device with relative error less than 10^{-4} . For the one-dimensional simulator, the total current density is discontinuous at the base contact which is taken to be at the midpoint of the base. For the emitter side, the current density is equal to the emitter current density and for the collector side it is assumed to be equal to the collector current density.

II.2 Physical Paramater models for Low Temperture Simulation

In the following section, we present the space charge model necessary to fully describe Poisson equation. A short description of the bandgap modeling parameters followed by the parameters used for scaling [5] are presented below.

II.2.1 Poisson's Equation

Poisson's equation which relates the electrostatic potential to the space charge can be represented as the sum of all the charges existing in the semiconductor [45]. This is given below as

$$\rho = q (p - n + N_D^+ - N_A^-) \quad (17)$$

where n and p represent the electron and hole concentrations, ρ is the potential. N_D^+ and N_A^- represent the ionized donor and acceptor concentrations respectively and are modeled by eqns. (31) and (32). For heavy doping concentrations the effective donor and acceptor ionization energies decrease as given in eqns. (33) and (34) for phosphorus and boron respectively [47]. An iterative solution techniques must be used to find N_D^+ and N_A^- that satisfy eqns. (1) - (6). E_{Fn} and E_{Fp} are the Quasi -Fermi levels and they are related to the electron and hole concentration through Fermi statistics. An iterative solution techniques must be used to find N_D^+ and N_A^- that satisfy eqns. (1) - (6). E_{Fn} and E_{Fp} are the Quasi -Fermi levels and they are related to the electron and hole concentration through Fermi statistics.

$$n = \frac{2 N_c}{\Delta F_1 \left[\frac{(E_{Fn} - E_c)}{kT} \right]} \quad (18)$$

$$p = \frac{2N_v}{\Delta F_1 \left[\frac{(E_v - E_{fp})}{kT} \right]} \quad (19)$$

N_c and N_v are the density of states in the conduction and valence band and are given by eqns. (20) and (21).

$$N_c = 2 \left(\frac{2\pi k T m_c^*}{h^2} \right)^{\frac{3}{2}} \quad (20)$$

$$N_v = 2 \left(\frac{2\pi k T m_v^*}{h^2} \right)^{\frac{3}{2}} \quad (21)$$

The effective masses for the electron m_c and hole m_v are modeled after the experimental work of Barber [15] and are valid over the temperature range of 50 - 300K.

$$m_c = 1.045 + 4.500 \times 10^{-4} T \quad (22)$$

$$m_v = 0.523 + 1.400 \times 10^{-3} T - 1.480 \times 10^{-6} T^2 \quad (23)$$

II.2.2 Apparant Bandgap Narrowing Modeling

The product of the equilibrium hole and electron concentrations (p_0 and n_0 respectively) in low doped semiconductors is a constant that depends only on temperature. That constant, n_{i0} is the intrinsic carrier concentration. The intrinsic concentration can be represented as a function of the density of states and the band width.

$$p_0 n_0 = n_{i0}^2(T) \quad (24)$$

$$n_{i0} = N_c N_v \left(\frac{-E_g}{2kT} \right) \quad (25)$$

The numerical model of n_i given by (25) is taken from Barber [15]

$$n_{i0} = 2.5 \times 10^{19} (m_c m_v)^{\frac{3}{4}} \left(\frac{T}{300} \right)^{1.5} \exp\left(\frac{-E_g}{2kT}\right) \quad (26)$$

The temperature dependence for the bandgap $E_g = E_c - E_v$ is taken from Bladau et al [3].

$$\text{For } T < 170\text{k}, \quad E_g = 1.170 + 1.059 \times 10^{-5} T - 6.050 \times 10^{-7} T^2 \quad (27)$$

$$\text{For } T > 170\text{k} \quad E_g = 1.179 - 9.025 \times 10^{-5} T - 3.050 \times 10^{-7} T^2 \quad (28)$$

In heavily doped material, there is an increase in the $p_0 n_0$ product due to substantial changes in the band structure. One way to treat the problem is to assume that a rigid narrowing of the bandgap is solely responsible for the increase of the minority-carrier concentration. The majority-carrier concentration is assumed to be equal to the ionized dopant concentration. In n-type silicon we can write,

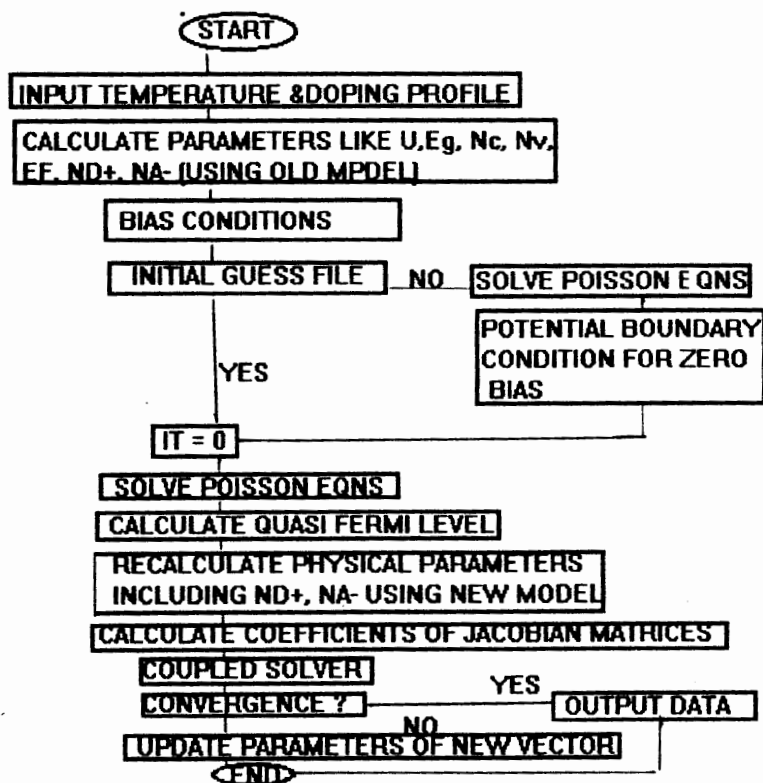


Figure 3. Flowchart for low temperature bipolar transistor simulations

$$p_0 N_D^+ = n_{ie}^2(N_D, T) = n_{io}^2 \exp\left[\frac{\Delta E_g^{app}(N_D, T)}{kT}\right] \quad (29)$$

where ΔE_g^{app} represents the bandgap shrinkage that would account for the increase of p_0 if no other heavy doping effect occurred. The model used in the original version of BiLow [5] is presented in section II.3.

II.2.3 Parameter Scaling

Since the dependent variables (U,n,p) in the basic semiconductor eqns (1)-(5) are of greatly different orders of magnitude and show a strongly different behaviour in regions with small and large space charge, the first step towards a structural analysis of these equations is appropriate scaling. Scaling, which was introduced by DeMari [12], transformed the basic semiconductor equations into a dimensionless form, which was convenient for computer simulation. The scaling factor proposed by Selberherr [45], for low temperature modeling, has been used in the original BiLow [5] and in the new version.

$$n_{inor} = 4.8 \times 10^{22} n_{i0} \quad (30)$$

the constant $4.8 \times 10^{22} \text{ cm}^{-3}$ is the number of silicon atoms per cubic centimeter and represents an absolute upper limit for the carrier concentration of any type. At 300K, $n_{inor} = 2.58 \times 10^{16} \text{ cm}^{-3}$ and at 77K, $n_{inor} = 3.51 \times 10^{11} \text{ cm}^{-3}$.

II.2.4 Transport Equations.

Another set of physical parameters that have to be carefully modeled for low temperature simulation consists of the transport and continuity equation parameters for both electrons and holes. The recombination rate R and the mobilities μ_n and μ_p are dependent on the semiconductor material, local values of carrier densities, electric field, and temperature. For the full set of equations, a relation between carrier density, the

corresponding quasi-Fermi potential, and electrostatic potential is needed. The models for recombination and mobility are given in details by Selberherr [45] and are used in the original BiLow [5].

II.3 Enhancement To The Simulator

In the original version of BiLow [5] low-temperature parameter models for the effects such as bandgap narrowing, impurity freeze-out, temperature and doping dependent mobility were developed and are described in [6]. In line with the progress in low-temperature simulation area [23], two main enhancements were made to the simulator program. In BiLow [5], the temperature dependence of the ionized impurity atoms N_A^- and N_D^+ was modeled by the classical formula using Fermi-Dirac statistic with appropriate degeneracy factors for electrons and holes in the conduction and valence band respectively.

$$N_D^+ = \frac{N_D}{1 + 2 \exp\left(\frac{E_{Fn} - E_D}{kT}\right)} \quad (31)$$

$$\text{and } N_A^- = \frac{N_A}{1 + 4 \exp\left(\frac{E_A - E_{Fp}}{kT}\right)} \quad (32)$$

Here the donor and the acceptor energies are given by

$$E_D = 0.045 - 3.6 \times 10^{-8} (N_{Tion})^{1/3} \quad (33)$$

$$E_A = 0.0438 - 4.08 \times 10^{-8} (N_{Tion})^{1/3} \quad (34)$$

$$\text{where } N_{Tion} = N_D^+ + N_A^- \quad (35)$$

Equations (31) and (32) fit measured ionized impurity concentration reasonably well for donor and acceptor impurity concentration less than or equal to 10^{17}cm^{-3} . However, at doping concentration greater than 10^{17}cm^{-3} , neighbouring impurities in the

semiconductor are now close enough that the electron wave functions at the impurity states overlap, resulting in their initial discrete energy levels changing into energy bands. Moreover, as a result of random distribution of impurities, fluctuations occur in the electrostatic potential throughout the crystal. Consequently there is some spatial fluctuation in the value of the conduction, valence, and impurity bands, thereby giving rise to bandtails. In addition, interaction between impurities and carriers and between ionized impurities and carriers results in the shift of the conduction, valence and impurity bands. From the simulation results using the original BiLow [5], one can see in Fig 4 the abrupt decrease in the electron concentration in the emitter, which becomes more visible at low-temperature. This is due to the improper modeling of Mott transition. Therefore, it has been our goal to study the ionization of impurity atoms and to come up with a continuous model to account for Mott transition. An improved ionization model based on Wieslaw Kuzmicz model [25] which accounts for incomplete ionization and Mott transition was verified at low-temperature and then added to the simulator. The Kuzmicz model along with the results are discussed in Chapter III.

Lot of discrepancies have been reported between the experimental [8]-[10], [26], [58] and theoretical [2], [20], [31], [32] and [46] data on bandgap narrowing(BGN). Bandgap narrowing is a very important factor in the current-gain (β) degradation and is responsible for the sharp decrease in β at low-temperature. Therefore, a better understanding and modeling of the bandgap narrowing parameter has to be developed. We have added two separate models for BGN in the temperature range of 300-77K. The expression for BGN used in the original simulator [5] is as follows

$$\Delta E_g = A \ln\left(\frac{N_T}{N_{ref}}\right) \quad (36)$$

where $N_{ref} = N_{ref}(300K) \frac{T}{300} \{1 + 2 \exp(\frac{E_{Fn} - E_D}{kT})\}$ for n-type

$$N_{ref} = N_{ref}(300K) \frac{T}{300} \left\{ 1 + 4 \exp\left(\frac{E_A - E_{Fp}}{kT}\right) \right\} \quad \text{for p-type} \quad (37)$$

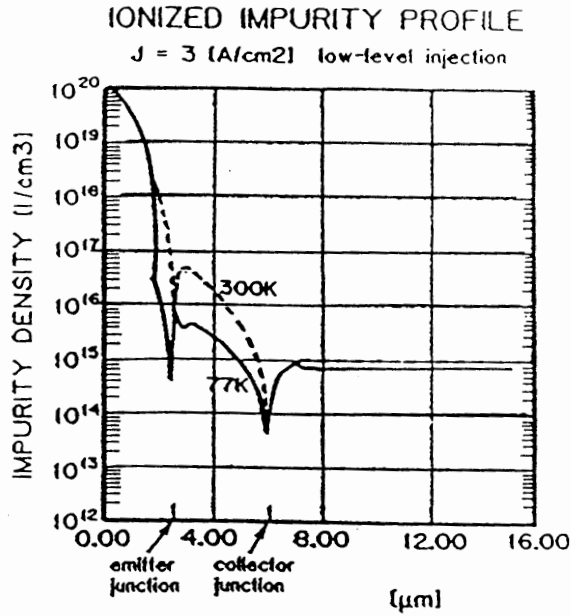


Figure 4. Total ionized dopant distribution in the transistor at T=300K and at T =77K [5]

A Fermi-Dirac correction was added to the above expression of BGN. The BGN models are presented in Chapter IV.

Chapter III

INCOMPLETE IONIZATION

III.1 Introduction

The incomplete ionization of a dopant impurity in a semiconductor is well understood for the case where the impurity concentration is low [23]. This is because the energy levels are discrete, and the band structure is unaltered from the intrinsic case [23]. It becomes fairly simple, then, to count the total number of available energy levels [23]. However at concentrations above 10^{18}cm^{-3} , several effects come into play making the situation more complicated. The initial discrete energy levels now change into energy bands because the electron wave functions at the impurity states overlap as the neighboring impurities get closer. As the impurity concentration increases, the impurity band enters the majority carrier band and the concept of separate band loses meaning. It then becomes critical to model this phenomenon properly. Theoretical calculations and measurements on silicon [23], indicate that in silicon in the doping range from about 10^{17}cm^{-3} to about 10^{19}cm^{-3} , at room temperature and below, the dopant atoms are not completely ionized and the majority carrier concentration is significantly less than the dopant concentration. It is usually assumed that all dopants at room temperature are ionized. At low temperatures this assumption may lead to significant inaccuracies in the prediction of the device parameters. Therefore, it becomes necessary to develop a relationship between the concentration of ionized dopant atoms and the total doping concentration. Klassen and De Graff [23] have calculated the percentage of ionized

dopant atoms in boron-doped Si and phosphorus-doped Si for the doping concentration upto $3 \times 10^{18} \text{cm}^{-3}$ and in the temperature range of 300K-50K.

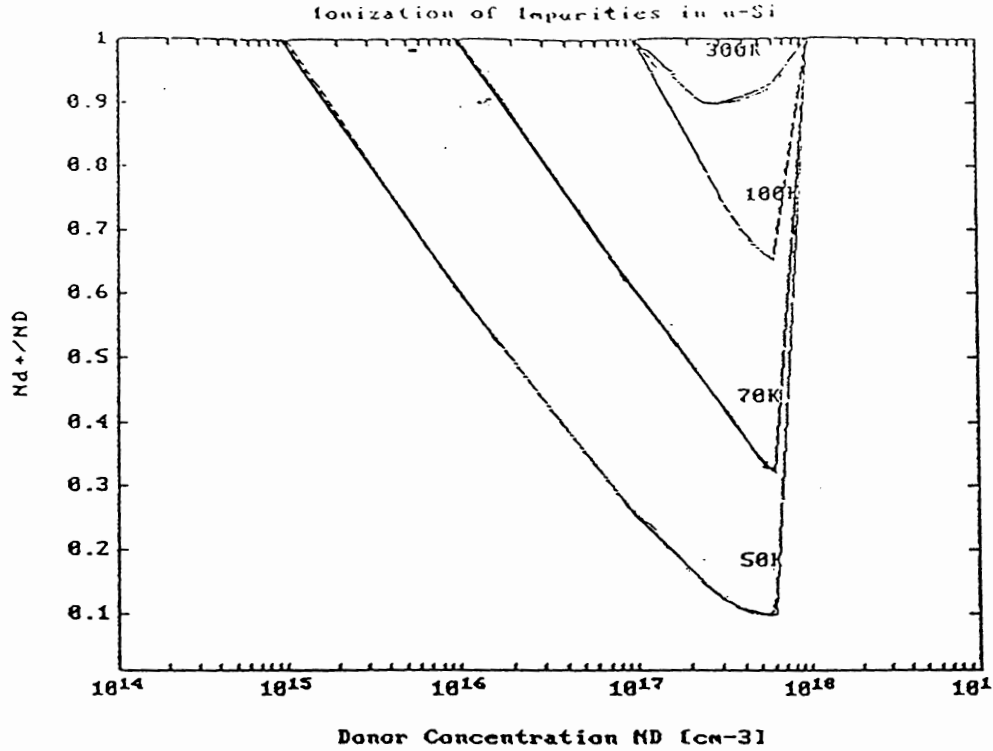


Figure 5. Ionized impurity ratio versus donor concentration [17]

The Klassen et al model [23] is based on a simple single-impurity-level theory, with the assumption that the activation energy of the donor or the acceptor level decreases with doping and eventually vanishes as the doping concentration approaches $3 \times 10^{18} \text{cm}^{-3}$. The model [23] uses eqns (31)-(35) to calculate the ionized donor and acceptor concentrations. The results from the Klassen et al [23] model are presented in Fig 5 which plots the ratio of ionized impurity to the donor concentration as a function of donor concentration for different temperature. As the doping concentration N_d increases, the ionization energy of the impurity level decreases [23] and complete ionization is achieved at donor concentration of 10^{18}cm^{-3} . Moreover, at high values of N_d there is the

broadening of the donor level into the impurity band, that will overlap with the conduction band. This effect enhances the ionization in such a way that for large doping concentration of 10^{18}cm^{-3} , ΔE_d in eqn (34) goes to zero and $N_d^+ = N_d$. In Fig 5 this can be clearly seen as complete ionization is achieved at 10^{18}cm^{-3} . The phenomenon of un-ionized impurity dopant is the strongest for doping distribution between 10^{17} and $3 \times 10^{18}\text{cm}^{-3}$. However, in this range of concentration the ionization is still incomplete. At the doping concentration that corresponds to zero activation energy, the concentration of ionized dopant atoms is neither equal to the total doping concentration nor temperature independent. Therefore, the model of Klassen and De Graff may be used at doping concentration well below 10^{18}cm^{-3} only.

To model incomplete ionization in the new version of BiLow we have selected the model proposed by Kuzmicz [25]. He has presented an analytical approximation for the ionized fraction as a function of temperature. This expression is shown [25] to be very useful in device modeling, especially for estimation of the temperature dependencies of device parameters. His results are based on a more sophisticated method and show complete ionization at high concentrations and can be used over a wide range of concentration. The model includes the change of activation energy with the doping as well as the broadening of the impurity level that results in the formation of the impurity band. The ionized impurity values obtained using Kuzmicz model [25] are in good agreement with those obtained from the model of Klassen and De Graff [23] for donor concentration upto 10^{18}cm^{-3} and in the temperature range from $T = 300\text{K}$ to $T = 50\text{K}$. Kuzmicz [25] has verified his theoretical ionization model with the experimental data obtained from the measurement of sheet resistance. He concludes that one cannot expect good agreement between resistivity calculations and experiment unless the incomplete ionization model is included. Since the model requires numerical solution of fairly

complex equation, we have used the analytical approximation presented by Kuzmicz [25].

III.2 Theoretical Modeling

In a semiconductor, the structure of energy bands depends on the doping concentration. The changes in this structure includes formation of the band tails, impurity band formation, merging of the impurity band with the majority carrier band and change of mean impurity activation energy with doping concentration due to the rigid shifts of the majority carrier band and/or impurity band. Experimental data [33], [57], and [36] indicate that the density of quantum states in the majority carrier band in heavily doped silicon is similar in form and magnitude to the density of quantum states in this band in pure silicon. This suggests that the band tail states have insignificant effect on the carrier concentration. The Kuzmicz model [25] described below, assumes that the density of quantum states in the majority carrier band is parabolic regardless of the doping concentration. The assumption is, however not sufficient to determine the total density of quantum states of free majority carriers. In lightly doped silicon the impurity bands are localized. As the doping concentration increases, the impurity bands enter the majority carrier band and eventually merge completely with this band. Thus, in heavily doped silicon the impurity levels become delocalized. The model assumes that in n-type semiconductor all donor levels below the edge of the conduction band E_C are localized, while the levels above E_C are delocalized. Similarly, in p-type semiconductor all acceptor levels above the edge of the valence band E_V are localized and the levels below E_V are delocalized. Therefore the model is based on the following assumptions:

- (a). It is assumed that in n-type silicon with total concentration of donors N_D and no acceptor atoms, the intrinsic concentration n_i is much less than the concentration of electrons in the conduction band.

(b). The density of quantum states in the conduction band $g_n(E)$ is parabolic regardless of the doping concentration.

$$g_n(E) = 2\pi \left(\frac{2m_n}{h^2}\right)(E - E_c)^{\frac{1}{2}} \quad \text{for } E > E_c, \quad (37)$$

where m_n is the effective mass and h is the Planck's constant, E_c denotes the conduction band edge.

(c). The density of quantum states in the impurity band g_d is [39] and [49]

$$g_d(E) = N_D (2\pi\sigma_e^2)^{-\frac{1}{2}} \exp\left[\frac{-(E - E_d)}{2\sigma_e^2}\right] \quad (38)$$

$$\text{where } \sigma_e = 1.036 \exp\left[-(11.3806 N_D^+)^{\frac{1}{2}} \lambda^{\frac{-3}{2}}\right] \quad (39)$$

and the parameters σ and λ are defined by

$$\sigma = q (N_D^+ / 8\pi\epsilon_s^2) \quad (40)$$

$$\text{and } \lambda = \frac{kT\epsilon_s}{q^2 N_D^+} \quad (41)$$

where N_D^+ denotes the concentration of ionized donors and the remaining symbols have their usual meaning. The above equations are consistent with the assumption that the total density of states in the impurity band equals the total concentration of donor atoms N_D

$$N_D = \int g_d(E) dE \quad (42)$$

(d). The donor activation energy is given by

$$\Delta E_d = E_c - E_d \quad (43)$$

and it is assumed that this energy decreases with doping according to $N^{1/3}$ law

$$\Delta E_d = \Delta E_{d0} - b(N_D)^{\frac{1}{3}} \quad (44)$$

$$\text{where } b = \frac{1.32 \times 10^{18} q^2}{\epsilon_0 \epsilon_s} \quad \text{ev.cm} \quad (45)$$

(e). The concentration of free electrons is given by

$$n_0 = \int_{E_c}^{\infty} [g_n(E) + g_d(E)] f(E) dE \quad (46)$$

where $f(E)$ denotes the Fermi-Dirac function

$$f(E) = \frac{1}{1 + \{\exp[\frac{(E - E_F)}{kT}]\}} \quad (47)$$

(f). The concentration of occupied localized states in the impurity band is given by

$$n_d = \int_{E_c}^{\infty} g_d(E) f_d(E) dE \quad (48)$$

where $f_d(E)$ is the probability function

$$F_d(E) = \frac{1}{1 + \{d_d \exp[\frac{(E - E_d)}{kT}]\}} \quad (49)$$

and the degeneracy factor for donors $d_d = 2$.

(g). Every donor atom introduces one donor level. Therefore, the number of occupied localized states equals the number of non-ionized donor atoms. It also follows from the neutrality condition that the concentration of free electrons equals the concentration of ionized donor atoms

$$N_D^+ = n_0 \quad (50)$$

Thus, the total donor concentration is given as

$$N_D = n_0 + n_d \quad (51)$$

$$N_D^+ = N_D - n_d \quad (52)$$

using eqns.(48) and (52), we get

$$N_D^+ = N_D - \int_{\infty}^{E_c} g_d(E) f_d(E) dE \quad (53)$$

using eqns (49) and (53),

$$N_D^+ = N_D \left[1 - \int_{\infty}^{E_c} \frac{g_{aux}(E - E_d)}{N_D f_d(E - E_d - KT \frac{(E_F - E_c)}{KT})} dE \right] \quad (54)$$

$$= N_D \left[1 - \int_{\infty}^{E_c} \frac{g_{aux}(E - E_d)}{N_D f_d(E - E_d - KTx)} dE \right] \quad (55)$$

where $g_{aux}(E - E_d)$ is an auxillary function defined by

$$g_{aux}(E - E_d) = \frac{g_d(E - E_d)}{N_D} \quad (56)$$

and x is an auxillary variable given by

$$x = \frac{(E_F - E_c)}{kT} \quad (57)$$

$$= N_D \left[1 - g_{aux}(E - E_d) f_d(E - E_F + \Delta E_d) dE \right] \quad (58)$$

Also

$$N_D^+ = n_0$$

substituting the value of n_0 from eqn (46),

$$N_D^+ = \int_{E_c}^{\infty} [g_n(E) + g_d(E)] f(E) dE \quad (59)$$

$$= \int_{E_c}^{\infty} g_n(E) f(E) dE + \int_{E_c}^{\infty} g_d(E) f(E) dE \quad (60)$$

Density of states in the conduction band

$$g_n(E) = 2 \times 2\pi \left(\frac{2m_n^*}{h^2} \right) (E - E_c)^{\frac{1}{2}}$$

Define a dimensionless variable,

$$y = \frac{(E - E_c)}{kT} \text{ and } z = \frac{(E - E_F)}{kT}$$

Therefore,

$$\int_{E_c}^{\infty} g_n(E) f(E) dE = 2 \times 2 \pi \left(\frac{2m_n^*}{h^2} \right)^{\frac{3}{2}} \int_{E_c}^{\infty} \frac{(E - E_c)^{1/2}}{[1 + \exp \frac{(E - E_F)}{KT}]} dE \quad (61)$$

$$= 2 \times 2 \pi \left(\frac{2m_n^*}{h^2} \right)^{\frac{3}{2}} (kT)^{\frac{3}{2}} \int_0^{\infty} \frac{y^{1/2}}{(1 + \exp(y - x))} dy \quad (62)$$

Fermi-Dirac integral of order 1/2 is

$$F_{1/2}(x) = \frac{2}{\pi} \int_0^{\infty} \frac{y^{1/2}}{(1 + \exp(y - x))} dy \quad (63)$$

$$\int_{E_c}^{\infty} g_n(E) f(E) dE = 2 \left(\frac{2\pi k T m_n^*}{h^2} \right)^{\frac{3}{2}} F_{1/2}(x) \quad (64)$$

$$\text{But } 2 \left(\frac{2\pi k T m_n^*}{h^2} \right)^{\frac{3}{2}} = N_c \quad (65)$$

$$\int_{E_c}^{\infty} g_d(E) f(E) dE = \int_{E_c}^{\infty} g_{aux}(E - E_d) f_d(E - E_F + \Delta E_F) dE \quad (66)$$

Substituting eqns.(64) in eqn (59), we get

$$ND^+ = N_c F_{1/2}(x) + N_D \int_{E_c}^{\infty} g_{aux}(E - E_d) f(E - E_c - KTx) dE \quad (67)$$

$$ND^+ = N_c F_{1/2}(x) + N_D \int_{E_c}^{\infty} g_{aux}(E - E_d) f(E - E_c - \Delta E_d) dE \quad (68)$$

where $g_d(E-E_d)$ is determined by eqns. (38)-(41). The system (60)-(68) may be solved numerically for x and N_D^+ provided that the total donor concentration N_D is given. Solving the system (60)-(68) for a set of values of N_D we obtain the concentration of ionized impurity carriers as a function of the total doping concentration.

The theoretical model of ionization of impurity atoms for p-type silicon is based on a similar set of assumptions. The only exception is the degeneracy factor in eq. (49).

$$d_a = 4 + 2 \exp(E-E_d / KT) \quad (69)$$

where $d = 44\text{meV}$

To facilitate the calculation of the fraction of ionized dopants, given below is an analytical approximation [25] which was used in the original simulator [5] to model the ionized impurity concentration..

$$\frac{N_D^+}{N_D} = 1 - A \exp\left\{-(B \ln\left[\frac{N}{N_o}\right])^2\right\} \quad (70)$$

Where A , B and N_o are constants and are determined as follows:

For n-type silicon,

$$A = 0.0824 T_n^{-1.622}$$

$$N_o = 1.6 \times 10^{18} T_n^{0.7267}$$

$$B = 0.4722 T_n^{0.0652} \quad \text{for } N < N_o$$

$$= 1.23 - 0.3162 T_n \quad \text{otherwise} \quad \text{Where } T_n = \frac{T}{300}$$

For p-type silicon,

$$A = 0.2364 T_n^{-1.474}$$

$$N_0 = 1.577 \times 10^{18} T_n^{0.46}$$

$$B = 0.433 T_n^{0.2213} \quad \text{for } N < N_0$$

$$= 1.268 - 0.338 T_n \quad \text{otherwise}$$

$$\text{Where } T_n = \frac{T}{300}$$

The above approximation has been evaluated at room temperature and at low temperature and then included in the simulator. Fig 6 shows the inclusion of the new ionization model into the simulator. The initial value of the ionized donor and acceptor concentrations are calculated using the original ionization model described in equation (31) and (32) in Chapter II along with the other physical parameters. After obtaining the initial guess file, the physical parameters are again updated. The flowchart of the simulator given in Fig 3 demonstrates this clearly. The recalculation of the ionized donor and acceptor impurity atoms is done after calculating the potential distribution and the Quasi-Fermi exponentials.

III.3 Results And Discussions

The results of the calculation using the analytical approximation based on Kuzmicz model [25] is shown in Fig 7 as a function of temperature and impurity concentration. From this figure it can be seen that complete ionization of impurities is achieved at concentrations greater than 10^{19}cm^{-3} . Comparing these results with those of Klassen and de Graff [23] in Fig 5, we see that even though the two plots show the same nature, complete ionization of impurity atoms is achieved by the Klassen et al [17] model at $N_d > 10^{18}\text{cm}^{-3}$. For values of donor concentration less than 10^{18}cm^{-3} there is a good agreement between the two models [23], [25]. However, the model of Klassen et al [23]

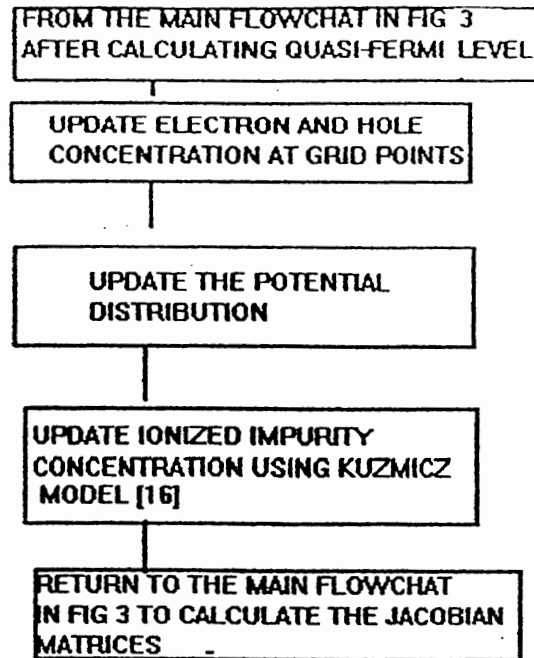


Figure 6. Incomplete ionization model flowchart

shows a discontinuity at high concentrations. The model [23] has been used largely for MOS device modeling and BJT modeling where the concentrations are low.

The Kuzmicz model was introduced in the new C-version of BiLow to model Mott transition and incomplete ionization and the continuity of the model was verified by comparing the net ionized impurity distribution at $T=77\text{K}$ and $T=300\text{K}$ as obtained from the simulation results using the original and new version of BiLow. The net ionized impurity distribution at $T=77\text{K}$ and at $T=300\text{K}$ for low level injection for the original BiLow is shown in Fig 1 and from the new version is shown in Fig 8. In both the figures one can see that the donor concentration in most of the emitter is above the Mott transition so the ionized donor concentration is not affected by freezeout. The concentration of acceptors in the base region and the concentration of donors in the collector region are both below the Mott transition, so they are temperature dependent and suffer from freezeout. In Fig 4 one can see the abrupt decrease in the electron and

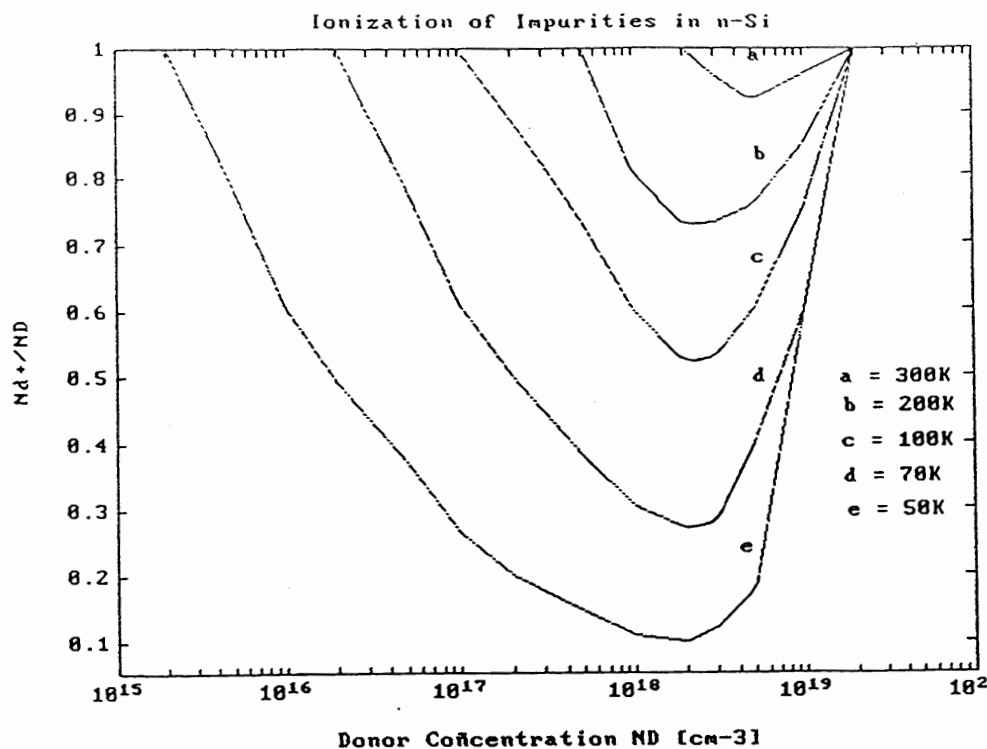


Figure 7. Ratio of the concentration of ionized impurity to the total doping concentration as a function of temperature and doping

concentration in the emitter at 77K. This is mainly due to the fact that there is a discontinuity in the Mott transition model at concentration around $3 \times 10^{18} \text{cm}^{-3}$. This discontinuity is removed in the new BiLow by using the model described in section III.2. In Fig 8 the doping profile has smoothen out at $T=77\text{K}$ with a continuity through the Mott transition region.

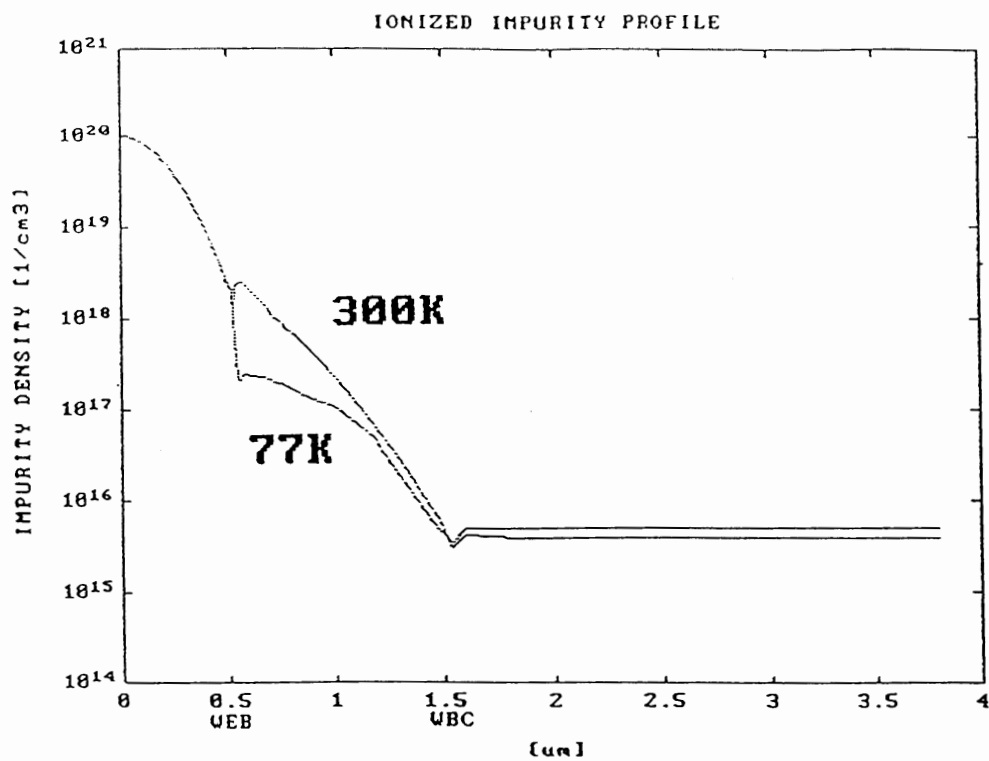


Figure 8. Total ionized dopant distribution in the transistor at $T = 300\text{K}$ and $T = 77\text{K}$ using the new BiLow

CHAPTER IV

BANDGAP NARROWING IN SILICON

IV.1 Background

Heavily doped regions are present in every semiconductor device of today. In addition to this, a reduction in the vertical dimensions for VLSI devices is also imperative [59]. The need to provide these thin, low resistive layers as well as tightly controlled depletion regions pushes the overall doping throughout the device to even higher levels. Because of its practical importance, extensive studies on heavily doped Si have been going on for a long time. Bandgap narrowing (BGN) which is the shrinkage of the energy gap at high doping concentrations is one parameter that has gained importance. The concentration of dopant impurities affect both the density of states associated with the host lattice as well as the density of states associated with the impurity atoms [60]. In heavily doped silicon, the energy band structure changes due to many body effect which results in the broadening of the impurity band and the band tail effect that results from the randomness of the impurity distribution on the edge of the conduction band and the valence band [60]. These effects impact the energy gap in the emitter and the base region. An increase in the bandgap narrowing, results in lower emitter efficiency and an increasing temperature dependence of the current gain [31]. However, the need to design higher performance devices requires a better understanding of the minority-carrier transport parameter in heavily doped silicon. Slotboom and De Graff [48] were among the first to fabricate devices designed to measure the influence of high doping on device performance. Following their work, a number of researchers have carried out both

experimental as well as theoretical work on what has become to be called "device bandgap narrowing", "apparent bandgap narrowing" or, simply "bandgap narrowing". In the following section we have tried to analyze the available literature on BGN in order to come up with a good model which is valid in the regions of low and high doping.

IV.2 Previous Work

The use of the name "bandgap narrowing" for denoting the parameter ΔE_g in device simulations is very unfortunate. Device measurements give the pn product in equilibrium, at most [34], [40], [51], and [55]. The extraction of the value of the semiconductor bandgap from the measurement of n_{ie}^2 requires a theoretical model and several assumptions regarding the density of states equation [9], for example, requires that Maxwell-Boltzman statistics are applicable, and that the density of states in the conduction and valence band be parabolic. The first assumption is clearly invalid. The other has not been proven. Therefore, it should not be surprising, that ΔE_g as obtained from a theoretical model and the resulting value of ΔE_g from optical and photoluminescence measurements are drastically different. Fig 9 presents the discrepancies of the BGN data, which were obtained from the electrical measurements of Slotboom et al [48], Weider [58], Mertens et al [33], and Nugroschel et al [36], and from the luminescence measurements of Dumke [13]. Additionally, part of the bandgap extraction problem lies in the way various workers have extracted n_{ie}^2 from I-V measurements, making assumptions that are not based on experimental observations. We have, therefore, grouped the literature survey into two, Group1 deals with the optical and electrical measurements and the Group2 deals with the theoretical derivations of BGN.

Group1 Experimental (Optical and Electrical Measurements): Most of the discrepancies in BGN values arise from the differences in the assumptions about mobility and, in some cases, from the lifetime data used in the interpretation of the experimental

data. During the last few years a number of experimental results have been published which show that, starting at a doping concentration of 10^{18}cm^{-3} , minority carrier mobility in silicon exceeds majority carrier mobility upto a factor of three at a concentration of 10^{20}cm^{-3} [24], [35] and [56].

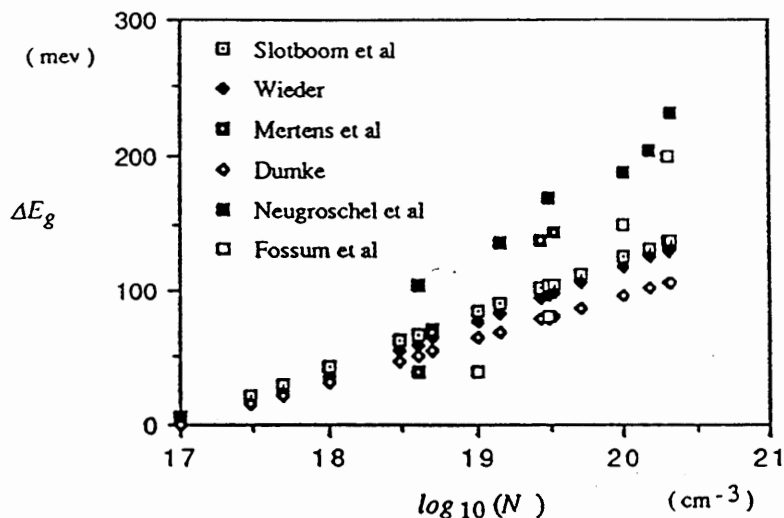


Figure 9. Summary of bandgap narrowing from different measurements.
 $N = [10^{17}, 2.1 \times 10^{20}]\text{cm}^{-3}$ [9]

Theoretical calculations have shown a long time ago that minority carrier mobility may exceed majority carrier mobility also at low-temperature [30] or at higher doping [41]. However, this did not result in the formulation of the mobility model that can be used in device simulation. Klassen [24] has presented an analytical model that unifies the description of majority carrier and minority carrier mobility and includes screening of the impurities by charge carriers, electron-hole scattering, clustering of impurities and the full temperature dependence of both majority and minority carrier mobilities. The excellent agreement between this model and published experimental data on the carrier mobility in silicon [56] ensures that this model is a sound basis for the revised

determination of bandgap narrowing work. The optical and experimental works on BGN are described separately in the following section.

Electrical Measurements: Klassen, Slotboom and De Graff [23] have used the Klassen mobility model [24] to recalculate the bandgap narrowing obtained from the experimental measurements upon which the most of the BGN models [48], [8]-[10] are based. The new bandgap narrowing value is given as the sum of the reported BGN value using the temperature independent $\Delta V_{go(N)}$ [48] plus the correction which includes the temperature dependence of mobility. The relationship between the new(corrected) value for the bandgap narrowing ΔV_{go}^{new} and the reported value ΔV_{go}^{rep} is found to be[23]

$$\Delta V_{go}^{new} = \Delta V_{go}^{rep} + \frac{kT}{q} \ln\left(\frac{\mu^{rep}}{\mu^{new}}\right) + \frac{kT}{q} \ln\left(\frac{C_1^{rep}}{C_2^{new}}\right) \quad (71)$$

The parameters used by different authors can be found in Table I [23]. Figure 9 shows the apparant bandgap narrowing as a function of impurity concentration. In the upper part, the new BGN value calculated from the Klassen et al. method [23] for p-type Si are smaller due to the minority electron mobility which is larger than the majority carrier mobility. Also for the data obtained by Slotboom et al. [48] with eqn.(71), the new value of BGN is smaller due to the difference in the temperature dependence between minority and majority carrier mobilities. Compared to the data reported by Del Alamo et al. [9]

Table I

Model parameters used in the interpretation of experimental data

	Slotboom and de Graff (p-type) [48]	Del Alamo et al (n-type) [9]	Swirhun et al. (p-type)[49]	Klassen et al. (n- p-type) [23]
$C_1(\text{cm}^{-6}\text{K}^{-3})$	9.61×10^{32}	1.38×10^{33}	1.26×10^{33}	9.61×10^{32}
$V_g(\text{mV})$	1.206	1.206	1.206	1.206
C_2	0.5	0	0.5	0.5

for n-type Si, the BGN values are larger. This is due to the difference in parameter C_1 for the intrinsic concentration at high concentration while it is due to the difference in the minority hole mobility and is clearly visible in the first row of the Table I for n and p-Si.

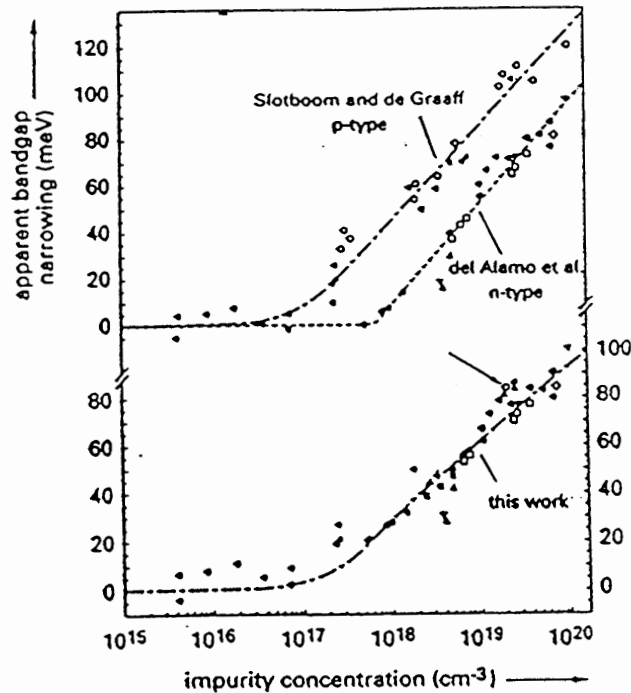


Figure 10. Bandgap narrowing as a function of impurity concentration [23]

In the lower part of Fig 10 the corrected value of BGN in both n- and p-type Si are given as a function of impurity concentration. The conclusion of Klassen et al. [23] was that all the new BGN values for n- and p-type Si lie close together and moreover, the bandgap narrowing can be shown to be temperature independent between 280-400K. The existing BGN model in the original BiLow [5] follows the Slotboom et al [48] model at room temperature. As such we have not taken the Klassen et al model [23], which is the same as Slotboom model plus a correction, to be included in the simulator. Swirhun et al [49] have measured the electron diffusion length and electron lifetime to extract the minority

carrier electron mobility as a function of doping density and concluded that the electron mobility is about 2.5 times larger in heavily doped p-type Si than in n-type. Using the Slotboom et al. model [48] they found 25meV more bandgap narrowing in p-Si than in equivalently doped n-Si. In their work of qualifying ΔE_g on N_A experimentally, King and Swanson [21] have attributed the various discrepancies on the assumption that minority carrier mobility equals majority carrier mobility and the value of n_{i0} at 300K used by different authors in extracting this parameter. Weybright and Plummer [57] have done a comparison of simulated Gummel plots with different BGN models. They concluded that King's BGN model [21] for p-type material gives the most satisfactory agreement over temperature, Swirhun's [49] and Slotboom's [48] models overestimate the collector current over temperature, but the Del Alamo's BGN model [10] for n- and p-type material gives the better fit to the measured data. Given in Table II are the BGN model parameters as used by Del Alamo [10], King [21], Slotboom [48] and Swirhun [49].

TABLE II
Comparison of BGN model parameters

BGN(effective) = $k \ln(N/N_c)$		
Model	K [meV]	N_c [cm ⁻³]
del Alamo	18.7	1.61e18
King	17.8	2.3e17
Slotboom	18.1	4.23e16
Swirhun	7.38	3.03e16

Kuzmicz and Wosik [27] performed steady state measurements on bipolar test structure and came up with a new approximation for the bandgap narrowing and minority carrier mobility in silicon, valid at all doping levels.

$$\Delta E_g = a n_m^{1/3} \quad (72)$$

where a is an empirical parameter calculated from the theoretical values of collector current and taken to be equal to $3.6 \times 10^{-8} \text{ eV-cm}$ and n_m is the majority carrier concentration. All measurements were performed at 300K. Kuzmicz and Wosik [27] then calculated the diffusion length from the bandgap and lifetime approximations proposed by them as well as used those proposed by Del Alamo [9]. The agreement was good for doping concentration below 10^{19} cm^{-3} but the difference increases quickly with doping. Based on other experimental comparison between their results and those of Del Alamo [9], Kuzmicz and Wosik [27] believe that their approximation agrees better with the experiment than do those of Del Alamo [9]. Our aim has been to study the different BGN models and to chose a model which is continuous over a wide range of donor concentration and can also be extended to low-temperature. The Kuzmicz and Wosik model claims to be valid for medium and high doping and could be extended to lower temepratures. We have therefore added this model in the simulator.

Optical Measurements: Wagner and Del Alamo [55] have compiled a set of Photoluminescence (PL) data [52]-[54] for the bandgap narrowing in n-type and p-type silicon at low temperature (20K) and at room temperature (300K). The room temperature PL data obtained from n-type material are compared to electrical data [9] for bandgap narrowing in heavily doped n- Si. The PL data are found to be fully consistent with low temperature selective absorption Photoluminescence excitation (PLE) measurements, which also cover n- and p-type material upto charge carrier concentration of $\sim 10^{20} \text{ cm}^{-3}$. The optical bandgap energy (due to indirect transition) remains almost constant over the whole range of concentration ($4 \times 10^{18} - 4 \times 10^{20} \text{ cm}^{-3}$) except for most heavily doped p-

type boron samples. This indicates that for the present range of carrier concentrations the band filling is almost canceled by the bandgap narrowing [7]. For the 300K spectra the direct extraction of optical bandgap is not possible due to the thermal spread of the carriers which smears out the high energy cutoff. Thus, Wagner and Del Alamo [55] calculated the optical bandgap at 300K from the 20K temperature data. A value of 60meV was added to the bandgap energy values of 20K to get the bandgap energy value at 300K, assuming that the temperature shift of the bandgap is independent of the doping level [8]. Within the experimental accuracy of $\pm 10\text{meV}$, the same BGN was found at 20K and at 300K [53]. Fig11 and Fig12 display ΔE_g as a function of carrier concentration at 20K and 300K.

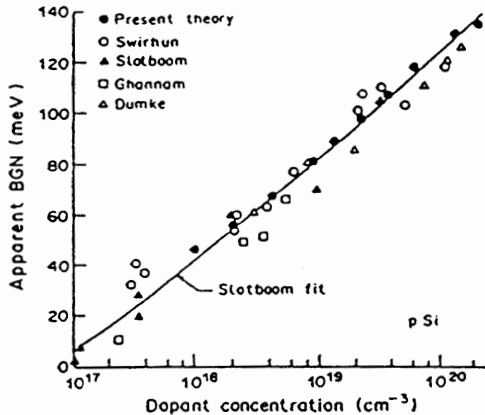


Figure 11. ΔE_g vs carrier concentration at 20K [55]

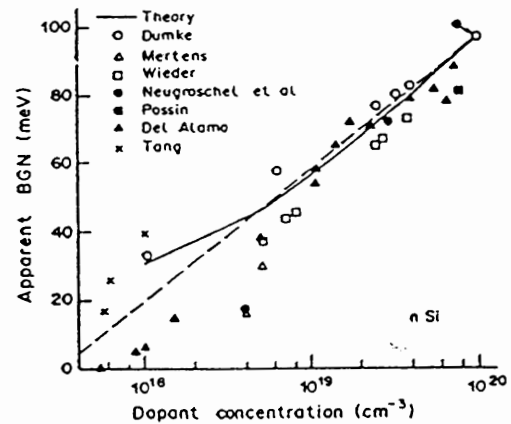


Figure 12. ΔE_g vs carrier concentration at 300K [55]

In the plots, the data reported by Dumke[13] was also compared. This data [13] was deduced from a line shape analysis. The data by Dumke [13] and the data of Wagner and Del Alamo [55] are in good agreement with each other. The data for BGN derived from

PL measurements were confirmed by selective absorption spectra recorded at low temperature using PLE spectroscopy. Based on the experimental data of [55] as well as by reinterpreting a number of data published in the literature [33], [57] Del Alamo, Swirhun and Swanson derived a consistent set of electrical data for bandgap narrowing in heavily doped n-type silicon [10].

$$\Delta E_g = 18.7 \times 10^{-3} \ln\left(\frac{N_D}{7 \times 10^{17}} \text{cm}^{-3}\right) \text{ [eV]} \quad \text{for } N_D > 10^{17} \text{cm}^{-3} \quad (73)$$

$$= 0 \quad \text{otherwise}$$

Boltzman statistics was used for the whole range of carrier concentrations. The agreement between the electrical [9] and the optical values [55] for the bandgap narrowing was surprisingly good considering the fact that completely different experimental techniques have been used. This implied that the bandgap narrowing measured by optical technique [55] was the same as the bandgap narrowing relevant to device properties. Both optical [55] and electrical data [9] were derived assuming parabolic bands for the majority and minority carriers. However band tailing of minority carrier band can affect the PL data for higher concentrations. Also the electrical data is expected to be sensitive to band tail states. Nevertheless, data obtained from both the techniques are in good agreement of each other. This suggests, either, that the density of states in the valence band tail is rather small or that the tails affect the electrical and the optical measurements in the same way. Previous efforts to correlate theoretical and experimental data are incomplete for a number of reasons [13]. These efforts were based on low-temperature PL data which were scaled upto room temperature. For n-type material with carrier concentration exceeding 10^{18}cm^{-3} , these scaled PL data gave a smaller bandgap narrowing [13] than the electrical data of Wieder [58] and Tang [51]. For p-type material a better agreement was found between the PL data and the transport data of Slotboom and de Graff [48]. However, the PL data in that case was not obtained

from a complete line shape analysis. They were deduced from the high energy cut off in the low temperature PL spectrum by subtracting calculated values for the band filling $E_v - E_f$.

From the optical [55] and experimental [48], [9], [49], [23] measurements, it can be concluded that the bandgap narrowing in n-and p-Si is the same. These measurements have also shown that BGN is independent of temperature.

Group2 (Theoretical Expressions): On the theoretical side, Mahan [32] using the many-body technique and Berggren and Sernelius [46] using the second order perturbation theory calculated the shift in the bandgap due to heavy doping. Mahans approach was elegant in that he used the many -body technique and still obtained a very simple expression for the BGN. However, since Mahan assumed the impurity atoms to be distributed in a periodic lattice in Si and Ge, his results for bandgap shift due to carrier impurity interaction were in error. The results of the theory of Berggren and Sernelius [46] are in reasonable agreement with the experimental results of Wagner and del Alamo [55] in the high density region where the theory is applicable. Since the theory of Mahan and Berggren et al. does not apply at high doping, we have not considered those models.

In previous years, calculations and experiments have been made which have shown that heavy doping effects strongly influence charge transport process in bipolar semiconductor device and that these effects must be taken into account for the modeling of the present-day structure. Therefore calculation of effective bandgap narrowing is important for electron and hole mobilities and densities in heavily doped Si. The calculations also show that effective bandgap narrowing is much larger in heavily doped and compensated than in uncompensated Si [33], [34] and [29]. Polsky and Rimshans [40] used a semiclassical approximation for the determination of effective bandgap narrowing in compensated cases. The Polsky BGN model was extracted using the linear screening theory of charges for doping concentration greater than 10^{23}cm^{-3} and for the

temperature range of $T=300-900\text{K}$. They concluded that the results of their calculations agreed sufficiently well with the measured data [40]. The Polskys BGN model was the only model that we found in the literature survey which talks about BGN in compensated material. A short description is provided above just for record. The Ploskys model [40] equation is complicated and cannot be directly used for simulation.

In their work, Jain and Roulston [20], discuss bandgap narrowing in high density regime (dopant concentration $> 1 \times 10^{18} \text{cm}^{-3}$) in n- and p-type Si and come up with an analytical expression for BGN as a function of majority carrier concentration at room temperature.

For n-Si,

$$\Delta E_g = 10.23 \left(\frac{N}{10^{18}} \right)^{\frac{1}{3}} + 13.12 \left(\frac{N}{10^{18}} \right)^{\frac{1}{4}} + 2.93 \left(\frac{N}{10^{18}} \right)^{\frac{1}{2}} \quad (74)$$

For p-Si,

$$\Delta E_g = 11.07 \left(\frac{N}{10^{18}} \right)^{\frac{1}{3}} + 15.17 \left(\frac{N}{10^{18}} \right)^{\frac{1}{4}} + 5.07 \left(\frac{N}{10^{18}} \right)^{\frac{1}{2}} \quad (75)$$

The semiconductor is assumed to be uncompensated and all impurities ionized so that N is also the concentration of free carriers. Values of bandgap narrowing using eqns (74)-(75) were found to be larger in p-Si than those in n-Si throughout the whole range of impurity concentration. Several researchers have attempted to determine the difference in BGN at room and at low temperatures. Since calculations at room temperature are complicated and the difference between room and low temperature BGN is small, reliable values of the temperature correction cannot be obtained. The high density theory used by Jain et al [20] to extract BGN at $T=300\text{K}$, is valid at low temperature. Therefore to extend their BGN model to low temperature, Jain et al. [20] have used a value between 0 and 20meV for this temperature correction [29], [54] and adopted the following procedure to do this. After applying the Femi-Dirac correction to the bandgap

narrowing, Jain et al. [20] have added a small constant temperature correction up to 20meV to the calculated values of BGN so as to bring their BGN results in agreement with the experimental value [13], [33], [36], [39], [49] and [57]. For p-Si, Jain et al [20] have added a temperature correction of 17meV. The results of the theory for p-Si with a temperature correction of 17meV are in good agreement with all experimental data and in almost exact agreement with the Slotboom fit extended by Dumke[13] and Del Alamo et al. [9] to higher concentration. The case of n-Si is less satisfactory. The optimum temperature correction is rather small, about 5meV. The spread in the experimental data points is much larger than in case of p-Si. At low concentration it is as much as 30meV. Fig 13 and Fig 14 show the calculated and the experimental BGN reported by Jain et al. [20] as a function of doping concentration.

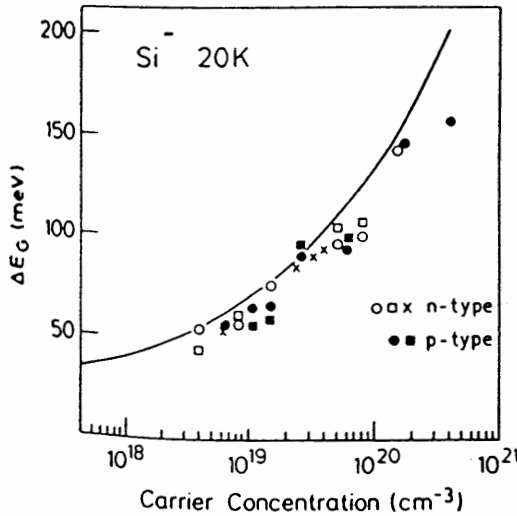


Figure 13. BGN values in p-Si [20]

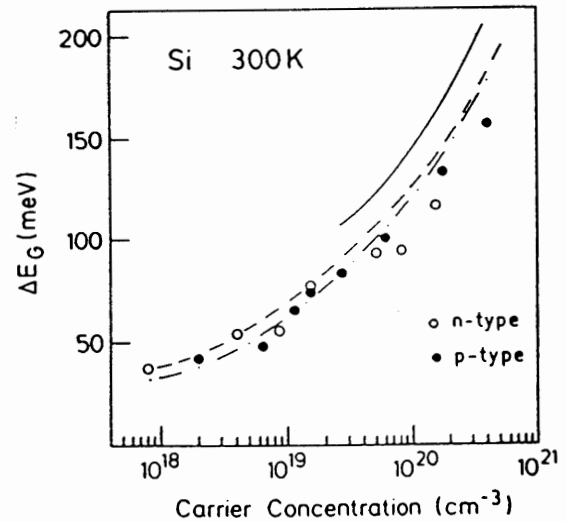


Figure 14. BGN values in n-Si [20]

Lanyon and Tuft [28] developed a theory of bandgap narrowing which takes into account the electrostatic energy of interaction between a minority carrier and a majority carrier surrounding it. This energy reduces the thermal energy required to create an

electron-hole pair. In contrast to the preceding theories which calculated the effects of high doping on individual conduction and valence bands, the Lanyon and Tuft [28] theory calculates a pair energy similar to the excitonic binding energy of bound electron-hole pairs in insulators. They calculate the pair energy and equate it to the bandgap reduction, thus reducing its activation energy.

$$\Delta E_g = \frac{3q^2}{16\pi\epsilon a_s} \quad (76)$$

where a_s is the screening radius and is given by $a_s = \left(\frac{q^2 n}{\epsilon k T}\right)^{-1/2}$

They concluded that at low doping levels, the temperature dependence of BGN and the square root dependence on the doping level can be seen. At higher doping levels above 10^{20}cm^{-3} the temperature independent $n^{1/6}$ dependence is followed. Comparing experimental data with theory, Lanyon and Tuft [28] have excluded all data that assumed in their interpretation that bandgap narrowing is independent of temperature except those of Neugroschel et al.[36] which are in the degenerate region 10^{20}cm^{-3} . The agreement between various workers is quite good on the whole. There are no adjustable parameters and there is no difference in the narrowing for n and p-type samples.

From Group 1, Wagner et al [55] and Klassen et al [23] along with other experimental works [9], [10], [48], [49] conclude that the bandgap narrowing in n-Si and p-Si is identical and is independent of temperature. From the theoretical group, Jain et al [20] have found that bandgap narrowing in p-Si is larger than that in n-Si for the entire range of impurity concentration. They also predict a possible temperature dependence of BGN and add a temperature correction to the BGN expression in order to obtain agreement at high concentration between their calculations and the uncorrected data at room temperature.

IV.3 Method Used In This Study

Experimental determination of BGN over temperature is limited and theoretical calculations have not been experimentally verified. This can be seen in the literature survey presented in Section IV.2. Surveying the BGN models described in group1 and group2, we selected two different models to be added to the simulator. The Del Alamo's BGN model [10] and the Kuzmicz model [27]. From Weybright and Plummer's [57] model comparison, it was found that Del Alamo's model gave a better fit to the measurement results for both n- and p-type materials at high doping concentrations. Besides the Del Alamo model is backed by PL work of Wagner et al. [55]. The model of Jain et al. [20] was neglected as it showed a lot of discrepancies between theory and experiment for p-Si. However, the claim of Kuzmicz and Wosik [27] that their approximation of BGN is valid for all doping levels was interesting enough to be considered as our goal was to study the effectiveness of the models for low, moderate and high doping concentrations. The original BGN model in BiLow [5] is similar to the Slotboom [48] BGN model at room temperature. However, at 77K, there is a non negligible difference between the two models due to incomplete ionization [23]. In the original model, the doping concentration at room temperature has been combined with the BGN temperature dependence from Lanyon and Tuft [28]. In Del Alamo et al [10] and Kuzmicz [15] BGN methods, the model parameters were calculated using the Fermi-Dirac statistics. The simulator uses the Fermi-Dirac statistics in its calculations, however the bandgap model parameters are usually extracted using Boltzman statistics. Therefore, it becomes necessary to represent the bandgap narrowing terms as a sum of the apparent bandgap plus the correction. We have modified the model used in the original version of BiLow to account for the Fermi-Dirac statistics. After reviewing literature on BGN, we have found that theoretical [32], [46], [28], work as well as experimental [8], [13], [27], [48], [55] and [52], measurements show that the BGN in n- and p-type Silicon is the

same. Therefore we have assumed in our work that the BGN is the same in n- and p- regions. Also, all the three BGN models that we have used, have been extracted using the majority carrier concentration. Since we have assumed that the majority carrier concentration is equal to the ionized concentration of dopant atoms, the ionized impurity concentration as obtained from our incomplete ionization model and shown in Fig 6 has also been used to calculate the bandgap narrowing at temperature as low as 77K.

IV.3.1 Method I: (del Alamo et al BGN model [8]): In lightly doped semiconductor, the product of the equilibrium hole and electron concentration (p_0 and n_0 , respectively) is a constant that depends only on temperature :

$$p_0 n_0 = n_{ie}^2(T) \quad (77)$$

where n_{ie} is the intrinsic carrier concentration.

. The net physical effect is the increase of the $n_0 p_0$ product. In other words, if a large number of electrons, N_D , are introduced , the resulting hole concentration in equilibrium is given by

$$p_0 = \frac{n_{ie}^2(T, N_D)}{N_D} \quad (78)$$

where $n_{ie} > n_{i0}$. n_{i0} is the intrinsic carrier concentration at equilibrium.

Therefore, p_0 in heavily doped silicon is higher than what could be from the eqn (77) for lightly doped semiconductor. Since n_{i0} depends exponentially on the bandgap of the semiconductor, a popular way of mathematically treating the problem is to assume that a rigid narrowing of the bandgap is solely responsible for the increase of p_0 , such that

$$p_0 N_D = \frac{n_{ie}^2(T, N_D)}{N_D} = n_{ie}^2 \exp \Delta E_g^{app} \frac{(N_D, T)}{kT} \quad (79)$$

where ΔE_g^{app} represents the fictitious bandgap shrinkage that would account for the increase of p_0 if no other heavy doping effects occurred. In this equation, complete

activation of dopant atoms is assumed. The following expression from Del Alamo et al [10] has been chosen to be added in the simulator.

$$\Delta E_g = 18.7 \times 10^{-3} \ln\left(\frac{N_D}{7 \times 10^{17}}\right) \text{ eV} \quad (80)$$

Because at low doping concentration the BGN model was extracted using Boltzman statistics, we modified the apparant bandgap narrowing model to accommodate the Fermi-Dirac statistics at high doping concentration by using the Joyce-Dixon approximation [19]:

$$\Delta E_g^{\text{appFD}} = \Delta E_{gB} + \Delta E_{gJD} \quad (81)$$

$$\Delta E_{gJD} = kT \left[0.353553 \left(\frac{N}{N_x}\right) - 4.95009 \times 10^{-3} \left(\frac{N}{N_x}\right)^2 \right] \quad (82)$$

ΔE_{gB} is the BGN extracted from equation (80), N is the majority carrier concentration, and N_x is the conduction band density of states for n-type material or the valence band density of states for p-type material.

IV.3.2 Method II: (Kuzmicz BGN model [27]): In this second method, we assumed that the density of states in the conduction band and the valence band to be parabolic and that the bandgap narrowing is identical in both n and p-type Si. Therefore the bandgap narrowing in heavily doped Si includes the "rigid" bandgap shift, ΔE_{geff} as well as an apparent bandgap widening due to the use of Fermi-Dirac statistics, ΔE_{gN} . Thus, the total BGN [27] is given by :

$$\Delta E_g = \Delta E_{geff} + \Delta E_{gN} \quad (83)$$

Kuzmicz and Wosik have assumed that the bandgap narrowing versus doping dependence can be approximated by the function :

$$\Delta E_{geff} = a n_m^{\frac{1}{3}} \quad (84)$$

where n_m is the concentration of majority carriers $n_0 = 1.0 \times 10^{17} \text{ cm}^{-3}$ and a is an empirical parameter. The estimate of a is done from

$$a = 1.32 \times 10^{18} \frac{q^2}{\epsilon_0 \epsilon_s} \text{ ev.cm} \quad (85)$$

This gives for Si ($\epsilon_s = 11.7$), $a = 3.6 \times 10^{-8} \text{ eV.cm}$.

The apparent bandgap narrowing ΔE_{gN} is given by

$$\Delta E_{gN} = kT \ln \left[\frac{\exp(\eta)}{F_{1/2}(x)} \right] \quad (86)$$

where $F_{1/2}$ - Fermi integral of order 1/2 and n is given by

$$R_n = \frac{n_m}{N_s} = F_{1/2}(\eta) \quad (87)$$

(n_m is the majority carrier concentration, N_s is the effective density of states in the majority carrier band).

$$N_s = N_c = 3.22 \times 10^{19} \left(\frac{T}{300} \right)^{\frac{1}{2}} \text{ cm}^{-3} \quad (88)$$

$$\text{and } N_s = N_v = 1.83 \times 10^{19} \left(\frac{T}{300} \right)^{\frac{1}{2}} \text{ cm}^{-3} \quad (89)$$

Density of states in the conduction band and in the valence band are assumed to be parabolic. Theoretically, a deviation from parabolicity can arise from several phenomena, including band tail formation and formation of impurity bands, which can merge into majority carrier band. However numerous experiments [8], [18] prove that the assumption of band parabolicity is sufficiently accurate in most cases. Thus ΔE_{gN} can be easily determined using a simple and accurate approximation of the Fermi-Dirac integral.

Equations (86) and (87) can be combined using the approximation of Nilsson [60] for the Fermi integral

$$\Delta E_{gN} = kT R_n [64 + 0.05524 R_n (64 + R_n)]^{-1/4} \quad (90)$$

IV.3.3.Method III (original BGN model in BiLow [5]): The following expression has been used in the original BiLow [5] for the purpose of modeling BGN

$$\Delta E_{gB} = A \ln\left(\frac{N}{N_{ref}}\right) \quad 18 \times 10^{-3} < A < 19 \times 10^{-3} \quad (91)$$

or n-type material

$$N_{ref} = N_{ref}(300K) \frac{T}{300K} \{1 + 2 \exp\left(\frac{E_{Fn} - E_D}{kT}\right)\} \quad (92)$$

For p-type material

$$N_{ref} = N_{ref}(300K) \frac{T}{300K} \{1 + 4 \exp\left(\frac{E_A - E_{Fp}}{kT}\right)\} \quad (93)$$

where $A = 18 \times 10^{-3}$ and $N_{ref}(300) = 7 \times 10^{17} \text{cm}^{-3}$ were used

Using the ΔE_{gJD} from eqn (82), we get

$$\Delta E_{g^{app}FD} = \Delta E_{gB} + \Delta E_{gJD} \quad (94)$$

$$\begin{aligned} \Delta E_{g^{app}FD} = A \ln\left(\frac{N}{N_{ref}}\right) + kT \left[0.353553 \left(\frac{N}{N_x}\right) \right. \\ \left. - 4.95009 \times 10^{-3} \left(\frac{N}{N_x}\right)^2 \right] \end{aligned} \quad (95)$$

IV.4 Results.

We have already shown in Fig 9 the collection of reported experimental data on $\Delta E_{g^{app}}$. We insist in denoting this parameter as " apparant bandgap narrowing " because of the way it is modeled through eqn.(79). The relationship between $\Delta E_{g^{app}}$ and any real shrinkage of the fundamental gap is far from clear. The dramatic discrepancies among theory [2], [20], [32] and [46], electrical [10] and luminescence [55] measurements indicate that a complete understanding of the band structure of heavily doped silicon is lacking. We have calculated the bandgap narrowing using Methods I, II

and III. Method I and Method II have been included in the simulator in the form of two separate subroutines. Temperature correction has been added to the existing Method III. However, the simulator program has the capacity to chose any model at a time. In Fig 15 we compare the room temperature values of bandgap narrowing ΔE_g as calculated using Method I, Method II and the original Method III from BiLow. The bandgap values calculated using Method II are larger than those from Method I. This discrepancy is

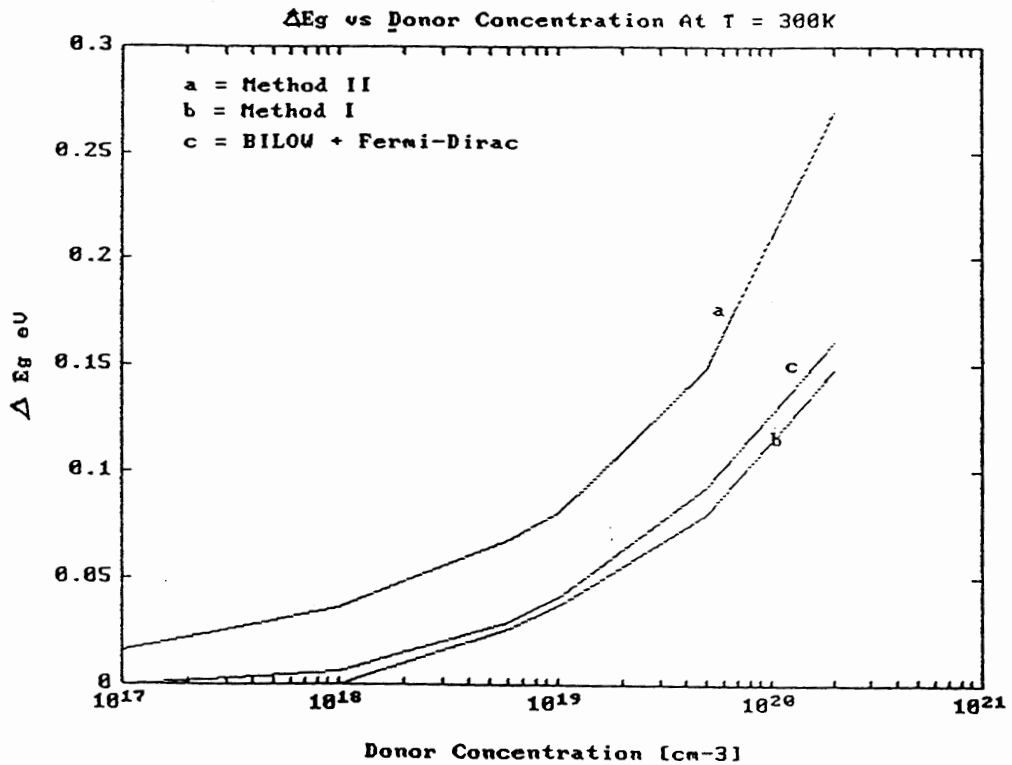


Figure 15. Bandgap narrowing at $T = 300K$ as a function of temperature

mainly due to the different assumptions made about the carrier mobility. In Method II, it is assumed that the mobility of minority electrons is equal to the mobility of majority electrons. Whereas, in Method I, it is assumed that the hole mobility in n-type silicon, as minority carrier, is about two times larger than as majority carrier in p-type silicon. It has

also been assumed in Methods I, II and III that all the dopant atoms are completely ionized and that the bandgap narrowing is the same in n-type and p-type silicon. Using the ionized fraction as a function of temperature and concentration, as obtained from our Mott transition model described in Chapter III, section III.2, we can now investigate the bandgap narrowing as a function of temperature.

In Fig 16, we have plotted the bandgap narrowing extracted from the models

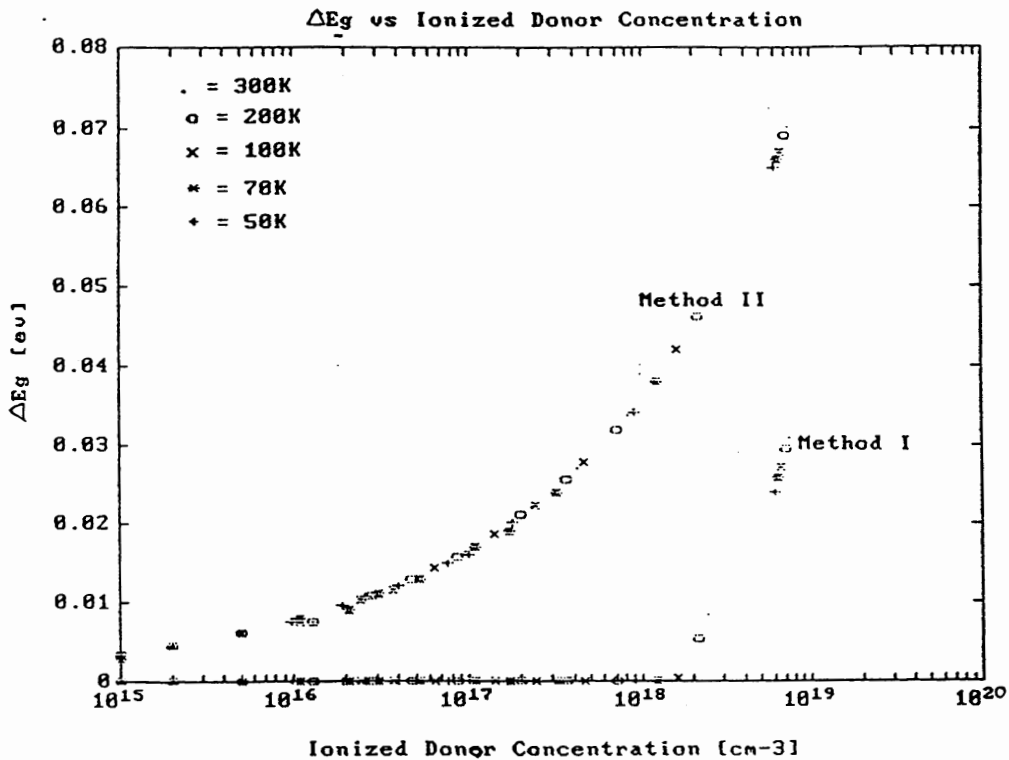


Figure 16. Bandgap narrowing as a function of ionized donor concentration and temperature

described above as a function of temperature and ionized impurity concentration. Within an accuracy of plus-minus 10meV, the same bandgap narrowing is found at 50K and at 300K. This is in good agreement with the optical PL results of J.Wagner et al. [55] and the experimental measurements of Klassen et al.[23]. The present values of the bandgap

narrowing have been derived assuming parabolic bands and do not include additional narrowing effects, which may arise from band tailing in the majority-carrier bands. Fig 17 shows the bandgap narrowing as a function of donor concentration which was obtained from the impurity ratio of the ionization model. In the literature survey on BGN,

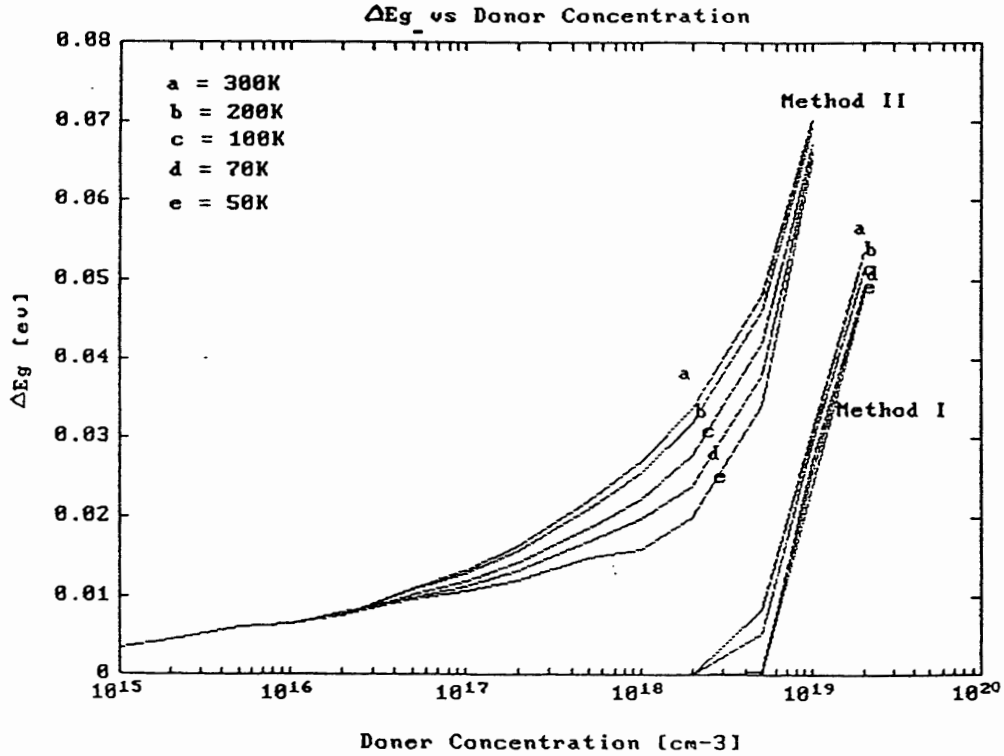


Figure 17. Bandgap narrowing as a function of donor concentration and temperature

we found that Jain and Roulston [20] have predicted a temperature dependency of BGN and have added a temperature correction to their BGN data. Comparing the BGN values from Fig17 with the theory of Jain et al [20] in Fig 14, we see that at low concentration the difference between the BGN values is as large as 30meV, but at high concentrations of about 10^{19}cm^{-3} the difference is only about 5meV. This may be due to the fact that all

in Fig 14, we see that at low concentration the difference between the BGN values is as large as 30meV, but at high concentrations of about 10^{19}cm^{-3} the difference is only about 5meV. This may be due to the fact that all interactions have not been properly taken into account in calculating the BGN in the rigid parabolic band approximation by Jain et al. [20]. The temperature dependence is dominant from 1×10^{17} to $5 \times 10^{18} \text{ cm}^{-3}$ for Method II and from 1×10^{18} to $2 \times 10^{19} \text{ cm}^{-3}$ for the Method I. In these ranges the impurity atoms are not ionized. The model described in Method I shows an abrupt increase in the bandgap at $4 \times 10^{18} \text{ cm}^{-3}$. This is not visible in Method II where the increase is gradual. Further, Method II is well described for low and moderate doping concentrations. One major difference between the two models is that Method I describes the BGN model at room temperature and extends it to lower temperatures. It assumes the density of states value at room temperature. In Method II, the effective density of states are described by the eqns (92), (93) and are temperature dependent. Simulations were done using the three models described in Section IV.3. The results of the simulations using the ionization model and the bandgap narrowing model are presented in the final Chapter V.

CHAPTER V

LOW TEMPERATURE BIPOLAR TRANSISTOR SIMULATION

V.1. Simulator Program Description

The simulator program, is a modified version of BiLow [5] converted from original FORTRAN-version to the C-version. It is a modular program that separates the algorithms that need to be experimented with, from the main body that does the iterative convergence needed to arrive at the solutions. It accepts user input interactively for parameters that determine the conditions of the program execution while using file input/output for most of the subsequent transfers. The input to the simulator is the simulation temperature, the doping profile and the initial guess file. The advantage of using the dynamic memory allocation facility makes the program consume less memory than the original. The simulator implementation is based on a variety of mathematical techniques [45]. Since the FORTRAN version of BiLow [5] used math functions from the FORTRAN library, new subroutines had to be written in the C-version to substitute for these standard FORTRAN options. Two modifications were made to the new program on the basis of the models which have been described in the earlier Chapters. The "make" file has been written to choose the model inclusion. Developed on a Sun Sparcstation, this program does not use any processor specific features and could be rebuilt on any platform where a C compiler is supported. The main BiLow program consists of about 3000 lines of code in addition to twenty subroutines.

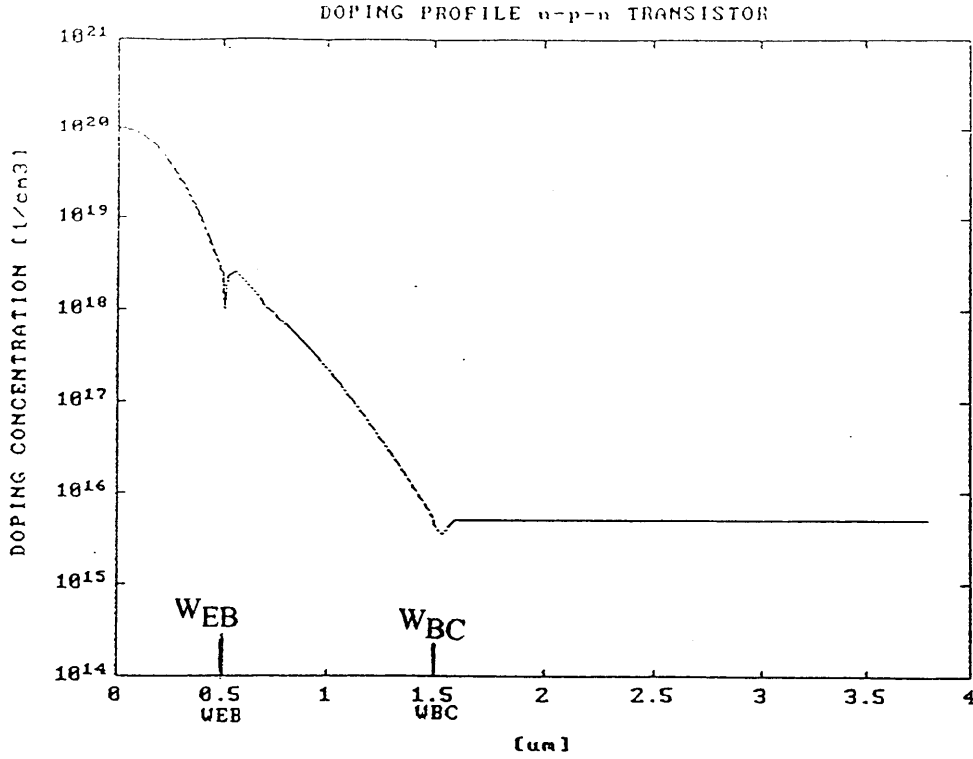


Figure 18. Doping profile of an n-p-n transistor T1

V.2. Simulator Performance

Two double diffused n-p-n bipolar transistor doping profiles were used in our simulations using the new BiLow. The profiles were generated using a double gaussian function given below

$$N(x) = N_e \exp\left(-\left(\frac{x}{x_e}\right)^2\right) - N_b \exp\left(-\left(\frac{x}{x_b}\right)^2\right) + N_{epi} \quad (96)$$

The doping profile of the transistor T1 neglects the contribution of the N_{epi} and is shown in Fig 18. The total number of grid points used in the simulator were 187 grid points and these can be further reduced or increased depending on the doping profile. The number of iterations needed for convergence depends on the doping profile, number of mesh points and their distribution, bias voltages, temperature, parameter models, and the algorithm used. Bias condition used for higher temperature had to be used as initial

conditions to obtain a solution for the temperature lower than 80K. Once convergence was obtained for a given temperature less than 80K, the solution was then used as the initial condition for the higher bias voltages as well as for lower temperatures. The flowchart for the operation of the simulator is given in Fig 3 of Chapter II. Table III presents the number of iterations that are needed to achieve convergence for different temperatures and different bias conditions. For large voltage steps, the simulator fails to converge. For voltage biases smaller than the maximum voltage showed in Table III, the number of iterations is the same as shown in Table III for a variety of voltage steps. The results in Table III are the same for transistors T1 and T2.

TABLE III
CONVERGENCE STATISTICS

Number of iterations	300k VBE [v]	150k VBE [v]	122k VBE [v]	100k VBE [v]	77k VBE [v]
- 300	< 0.84	< 0.9	< 0.9	< 1.00	< 1.00

V.3. Simulator Results

Sample results of bipolar transistor simulation in the active region in which the emitter-base junction is forward biased and the base-collector junction is reverse biased are presented below. The simulations were performed by including the incomplete ionization model described in Chapter III and the bandgap narrowing model of Method II given in Chapter IV. Simulations were also performed using the other two BGN models, Method I and Method III presented in Chapter IV. The charge characteristics versus current densities for 300-100K temperature

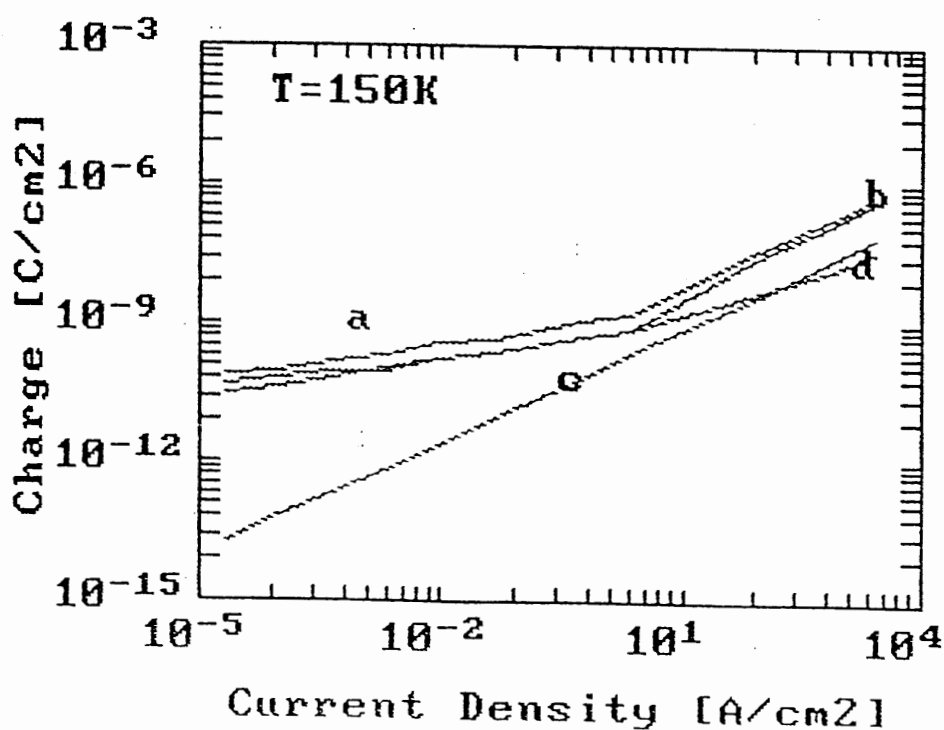
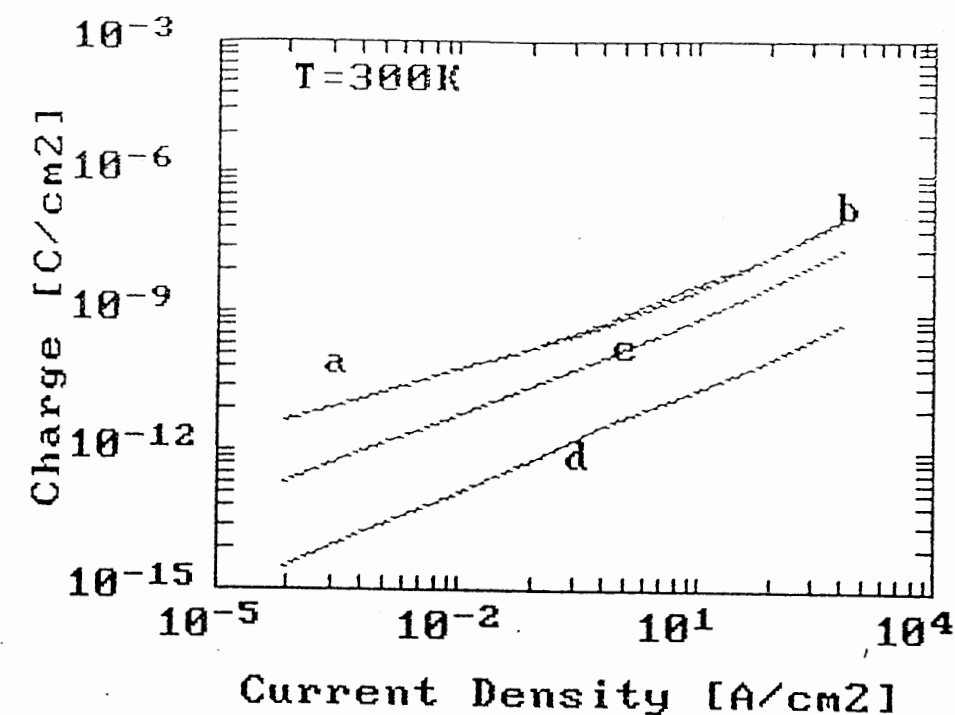


Figure 19. Charge Characteristics at (a) T=300K and (b) T=150K. a=total charge, b= charge of electrons in base, c= charge of holes in emitter, d= charge of donors trapped in base.

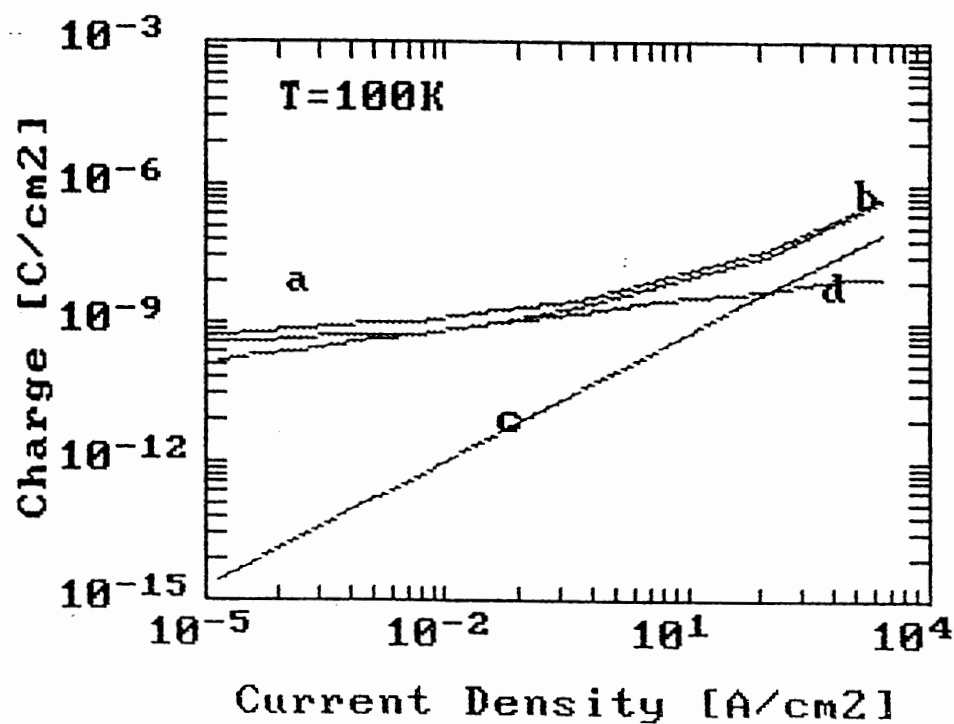
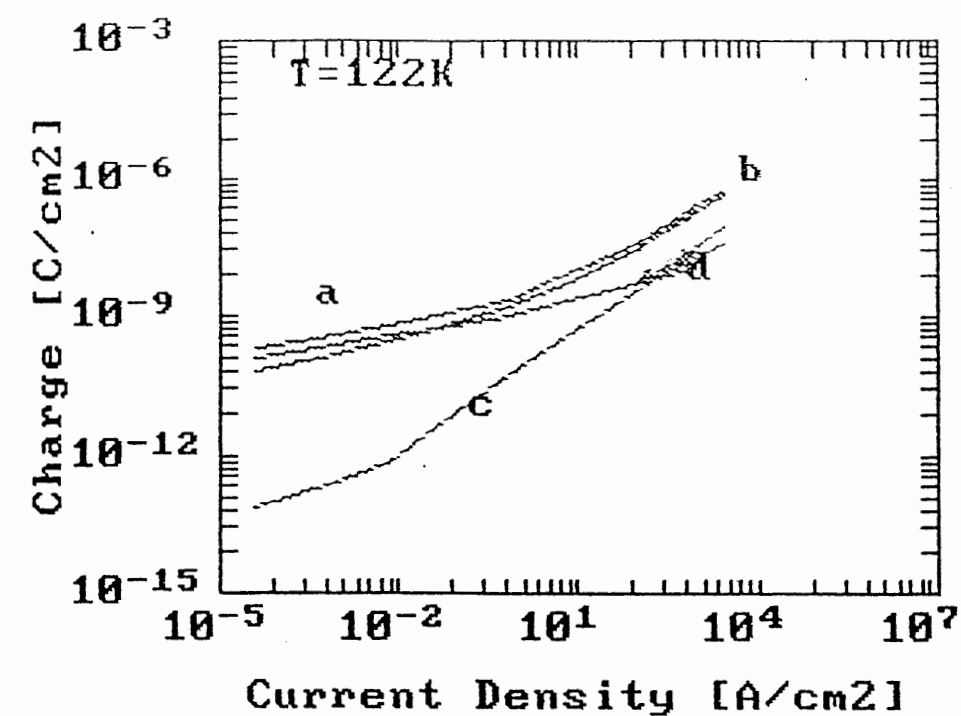


Figure 20. Charge Characteristics at (a) $T=122\text{K}$ and (b) $T=100\text{K}$. a = total charge, b = charge of electrons in base, c = charge of holes in emitter, d = charge of donors trapped in base.

range were generated. Fig 19a, Fig 19b, Fig 20a, Fig 20b show the data for $T = 300\text{K}$, $T = 150\text{K}$, $T = 122\text{K}$ and at $T = 100\text{K}$ for the transistor T1 respectively. Under the "active region" bias conditions holes will be injected into the n-type emitter from the base; electrons will be injected from the n-type emitter into the p-type base. Fig 21 shows the minority carrier distributions in an n-p-n transistor. The x-axis represents the distance along the length of the device. The origin is chosen where the active base begins. The end of the active base, at the base edge of the collector depletion region, is at $x = w$. The other edges of the two depletion regions are at $x = -x_E$ for the emitter and $x = x_C$ for the collector. The distribution of the minority carriers across the device is shown assuming low level injection condition. Since the base-collector region is reversed biased, the minority carrier concentration on each side of this depletion layer is zero. The collector acts as a "sink" for the electrons diffusing across the base. The electric field within the base-collector space charge region will carry the electrons through to the N type collector giving rise to a collector current I_C which is as large as the forward-bias current of the emitter-base junction I_E .

To explain the charge characteristics as a function of current density we have drawn in Fig 22(a)-(g), the minority carrier distribution in the emitter and the base of an n-p-n transistor with homogeneous doping profile for high and low injection levels. The plots are for increasing base-emitter voltage and a constant reverse bias voltage on the base-collector junction. The electron and hole concentration in the base and the emitter are $n(x)$ and $p(x)$ respectively. At $T = 300\text{K}$, and at small bias voltage on the base-emitter junction, the total charge consists mainly of the electron injected into the base whereas the hole current is negligible and this can be seen in Fig 22(b). However at high current density, space charge region in the base increases and moves towards the collector, increasing the base width. The current in the base region increases and the slope of the concentration is steeper than that at low level injection condition.

When the collector current is low the electrons crossing the C-B junction are less in number leading to a low level injection condition. However, as the current increases, the density of electrons transported across the C-B junction increases and at some point becomes comparable to the doping on the collector side of the space charge region. this condition is known as high level injection condition.

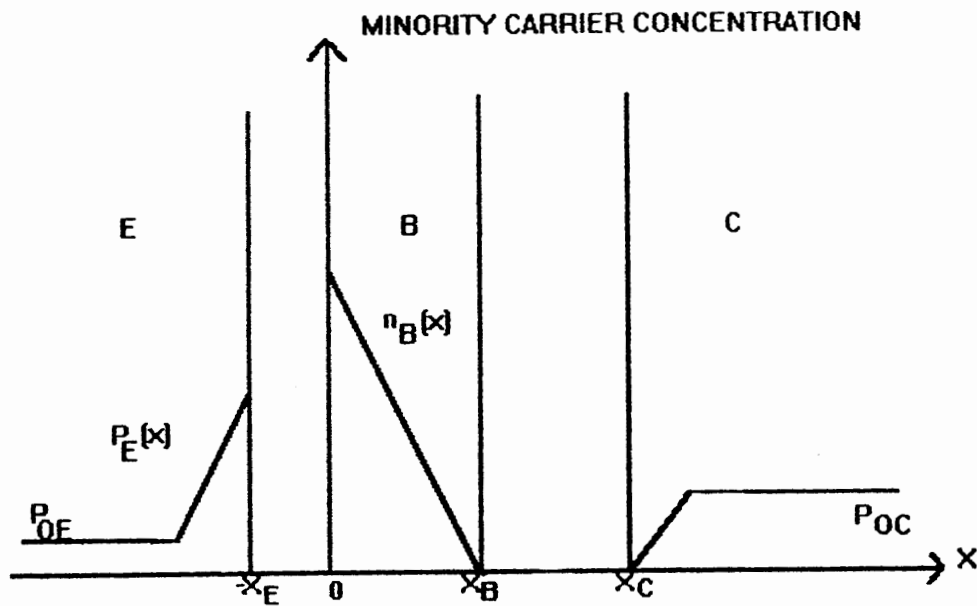


Figure 21. Minority carrier distribution in n-p-n transistor

This is shown in Fig 22(c) and the total charge increases but is still mainly composed of the charge of the electrons injected into the base. In Fig 20a, at $T=122K$, the total charge at low level injection condition is larger than the total charge at $T=300K$ and at the same collector current. This is because at $T=122K$ and for small voltages, the total charge is the sum of the free electrons and the electrons trapped on the donor sites in the base. In Fig 22(d) the slope of the minority concentration in the base remains the same as in Fig 22(b), even though the trapped electron concentration increases. There is also a small increase in the hole concentration due to back injection of holes into the emitter. As the

current density increases, the total charge is now the sum of the charges of the trapped electrons plus the free electrons in the base as well as the charge of the holes back injected into the emitter. Fig 22(e) shows the minority carrier distribution at higher collector current density. The charge distribution at $T=77K$ can be explained using Fig 22(f) and Fig 22(g). In Fig 22(f), the condition for low current density shows that the total charge is now mainly due to the electrons trapped on the donor sites in the base and the holes in the emitter, whereas the total charge at high injection condition in Fig 22(g) is mainly due to the holes back injected into the emitter. Comparing the charge characteristics plots of Fig 19(a)-(b) and Fig 20(a)-(b) with those obtained using the original BiLow [6] shown in Fig 24, Fig 25, and Fig 26, we see that the same trend as seen in Fig 19 through Fig 20, continues.

Explanation of charge characteristics

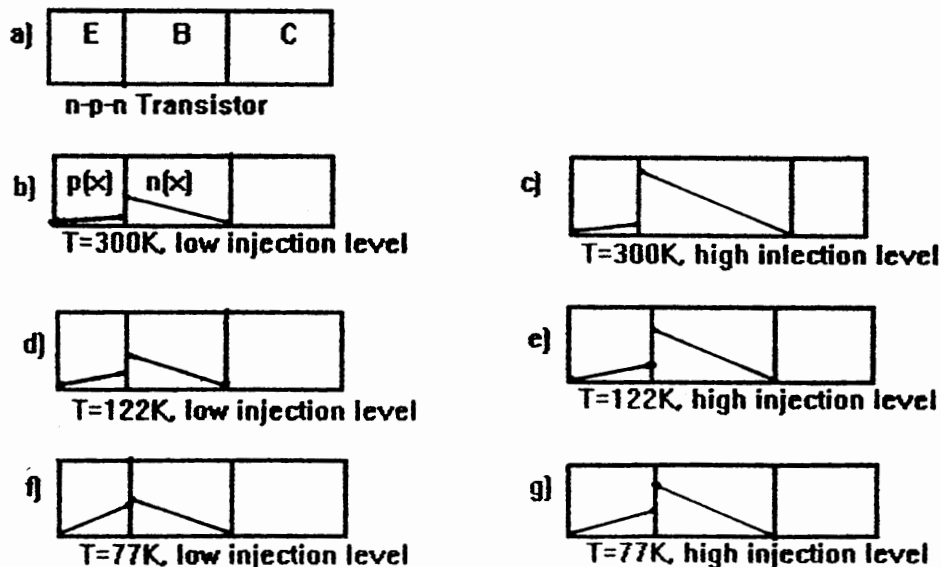


Figure 22 Minority carrier distribution to explain the charge characteristics

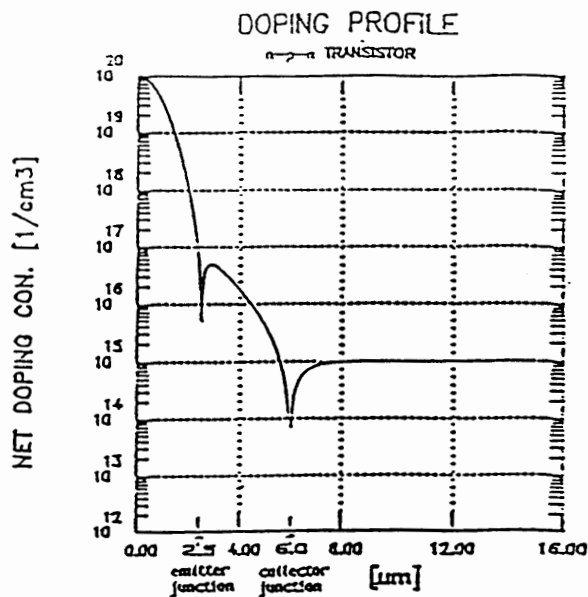


Figure 23. n-p-n doping profile [6]

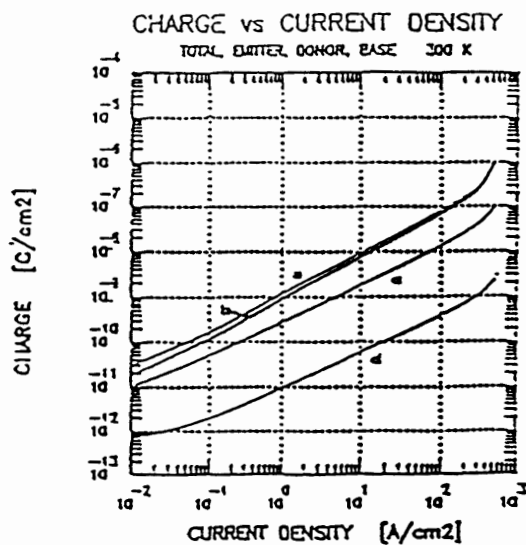


Figure 24. Charge characteristics at $T=300K$ [6]

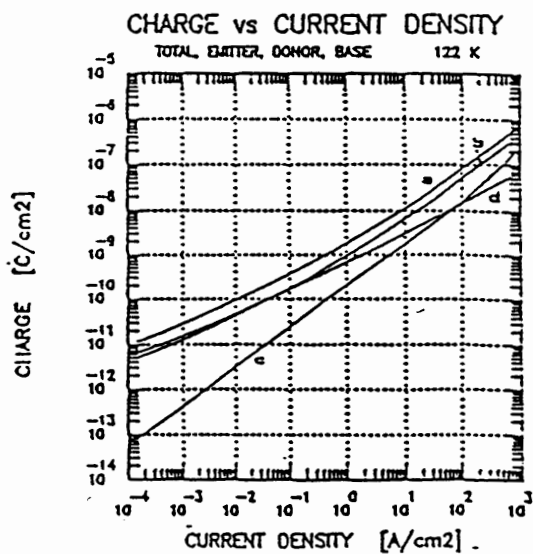


Figure 25. Charge characteristics at $T=122K$ [6]

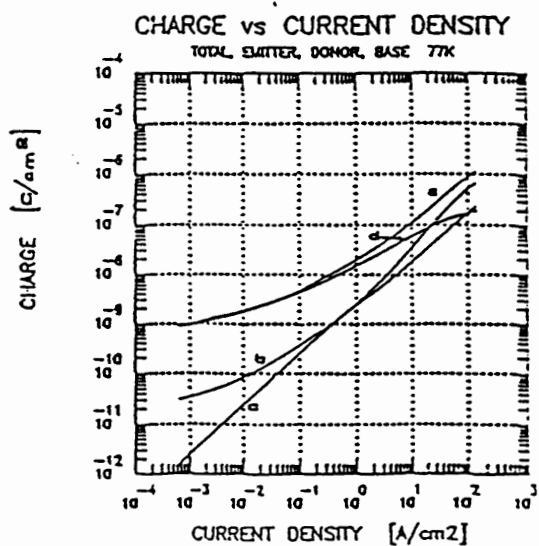


Figure 26. Charge characteristic at $T=77K$ [6]

At $T=300\text{K}$, the charge of the free electrons in the base of T1 in Fig 19(a) is larger than the charge in Fig 24. At $T=122\text{K}$ in Fig 20(a), the total charge in the transistor T1 is larger than the total charge of Fig 25. Comparing the doping profiles of transistor T1 from Fig 18 and the profile in Fig 23, we see that the total charge in the transistor which depends on the doping concentration in the emitter and the base is larger in case of transistor T1. This is due to the smaller size as well as higher doping in the base and the emitter of T1 as compared to the doping profile of the transistor shown in Fig 23. Electron distribution for high-level injection conditions are plotted in Fig 27. As the collector current density in an n-p-n transistor increases, the density of electrons being transported across the C-B space-charge region also increases. When this density becomes comparable to the doping on the collector side of the space-charge region, the total charge in this region becomes significantly reduced, leading to a lower electric-field gradient in the C-B junction. With a lower maximum electric field strength, the space-charge region edge in the base moves towards the collector, effectively increasing the base width. This phenomenon is known as base push-out effect apparent at $T=300\text{K}$ and at high injection level. In addition, larger base width also reduces the f_T and the transistor speed. At low temperature and at low current densities, the decrease of f_T can be related to the increase in the charge of the electrons trapped at the donor sites. At higher current densities and at low temperature, the decrease of f_T is mainly due to the increase in the charge of holes back injected into the emitter. Fig 28 gives the electron concentration for high level injection conditions as reported by Chrzanowska-Jeske and Jaeger [6]. On comparing Fig 27 and Fig 28, we see that the base push out effect which is prominent at $T=300\text{K}$ in both cases has vanished at lower temperatures. This is because as the temperature goes down, the number of free electrons in the base get trapped on the donor sites, and the density of electrons being transported across the C-B space-charge region decreases and there is no more increase in the base width. Therefore, at room temperature and at high current densities, the base

push out effect contributes to the decrease in f_T because of the increase in the base width which affects the base transient time. As the temperature decreases the base push out effect vanishes and the decrease in f_T at low injection levels is now mainly due to the charge of the trapped electrons in the base. At high injection levels and at low temperatures the decrease in f_T is due to the increase in the charge of the holes back injected into the emitter.

Another important characteristic of the bipolar transistor is its ability to provide current gain at high frequency. The unity gain frequency is the frequency of the transistor at which the current gain decreases to unity and it can be related to the physical structure of the transistor through the transient time τ_{ec} of the transistor [6]

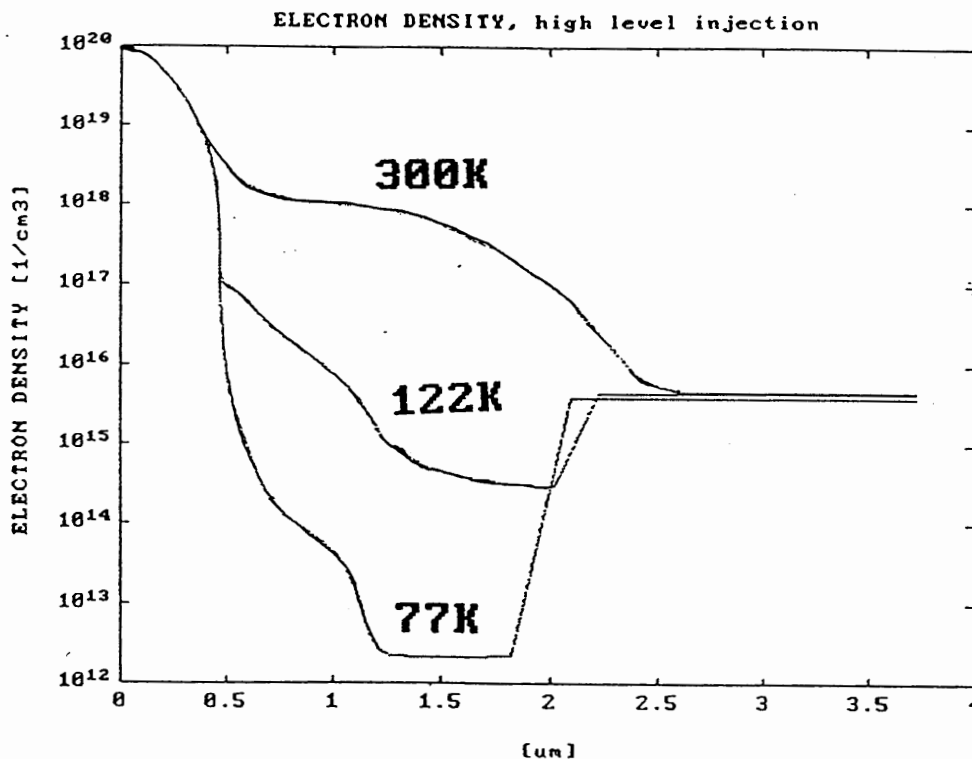


Figure 27. Electron concentration at different temperatures for high level injection: $J(300\text{K}) = 480\text{A}/\text{cm}^2$, $J(122\text{K}) = 800\text{A}/\text{cm}^2$, $J(100\text{K}) = 65\text{A}/\text{cm}^2$

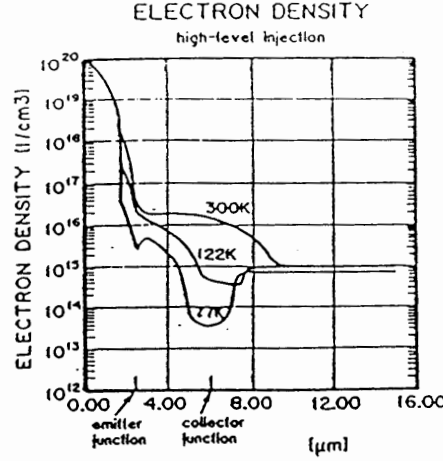


Figure 28. Electron concentration at different temperatures for high level injection: $J(300K) = 550A/cm^2$, $J(122K) = 840A/cm^2$, $J(77K) = 65A/cm^2$ [6]

$$f_T = 1/2 \pi \tau_{ec} \quad (97)$$

The delay time τ_{ec} is the sum of the emitter storage time and the base transit time.

The emitter storage time is given by [6]

$$\tau_e = \frac{Q_p}{I_E} \quad Q_p \text{ -holes in the emitter} \quad (98)$$

The base transient time is given by [6]

$$\tau_B = \frac{(Q_{nf} + Q_{nt})}{I_c} \quad (99)$$

Q_{nf} is the total charge of free electrons in the base and Q_{nt} is the charge of the electrons trapped on the donor sites. For minimum value of τ to be obtained, several features of the bipolar transistor structure have to be optimized. The base width should be as narrow as possible, buried layers and deep-collector contacts are used to minimize R_C . The simulated temperature dependence of the peak f_T is shown in Fig.29. From our simulated results, the peak f_T is a function of temperature with a maximum around $T=150K$. From Fig 29, it looks like the temperature dependence of the peak f_T occurs at temperature

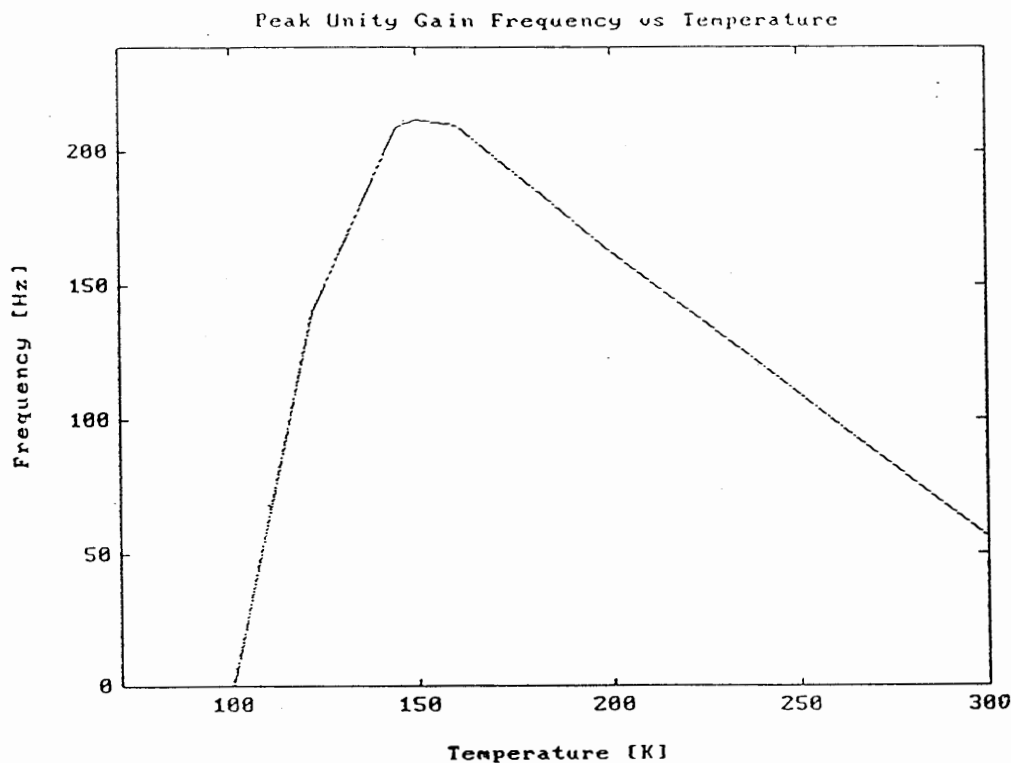


Figure 29. Temperature dependence of peak f_T for T1

$T=150\text{K}$. The decrease in the base width with the decrease in temperature decreases the base transient time, resulting in the decrease of the unity gain frequency at around 150K . For a matter of comparison, we have shown the plot of unity gain frequency versus temperature obtained from the previously reported results using the old BiLow [6] in Fig 30. The unity gain frequency values at $T=300\text{K}$ of Fig 29 is smaller as compared to the corresponding value in Fig 30. This may be due to the base push out at $T=300\text{K}$. Since the doping in the base is higher in the new transistor T1 than in the original profile of the Fig 23, there is more base push out in T1 and therefore lower f_T at room temperature. The unity gain frequency as a function of temperature and collector current density is shown in Fig 31. Fig 32 plots the f_T as a function of current density as given by the old BiLow [5]. In Fig 32, the unity gain frequency at $T=300\text{K}$ is lower than the frequency at $T=122\text{K}$ whereas in Fig 31, the f_T at $T=300\text{K}$ lies between the frequency at $T=150\text{K}$ as

shown in Fig 31. Fig 32 plots the f_T as a function of current density as given by the old BiLow [5]. In Fig 32, the unity gain frequency at $T=300\text{K}$ is lower than the frequency at $T=122\text{K}$ whereas in Fig 31, the f_T at $T=300\text{K}$ lies between the frequency at $T=150\text{K}$ and $T=122\text{K}$. Again we can relate this to the higher doping and smaller size of the transistor T1. In Fig 31, f_T begins to degrade rapidly for temperature below 122K . The sharp decline of the peak f_T at low temperature can be explained by the high ratio of the charge trapped on the donor in the base to the charge of the free electrons in the base. The simulated temperature dependence of this ratio, calculated at the peak f_T is shown in Fig 33. It was also observed, from the simulated results, that the total charge at low current densities decreases strongly with an increase in temperature whereas at high current densities it is a weak function of temperature.

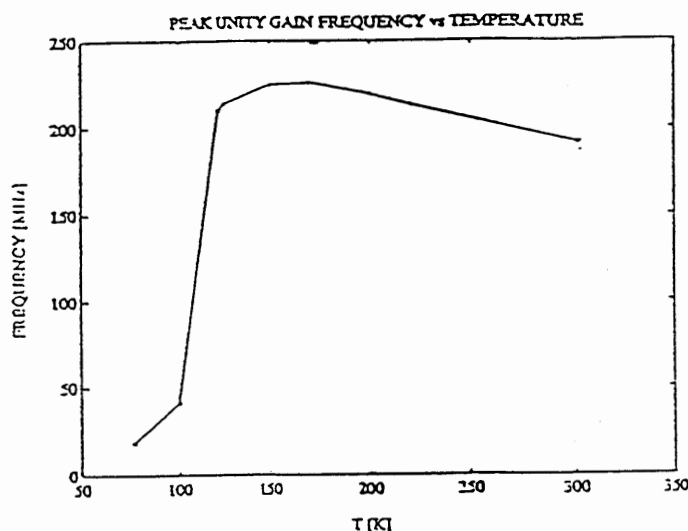


Figure 30. Simulated temperature dependence of peak f_T for n-p-n transistor [5]

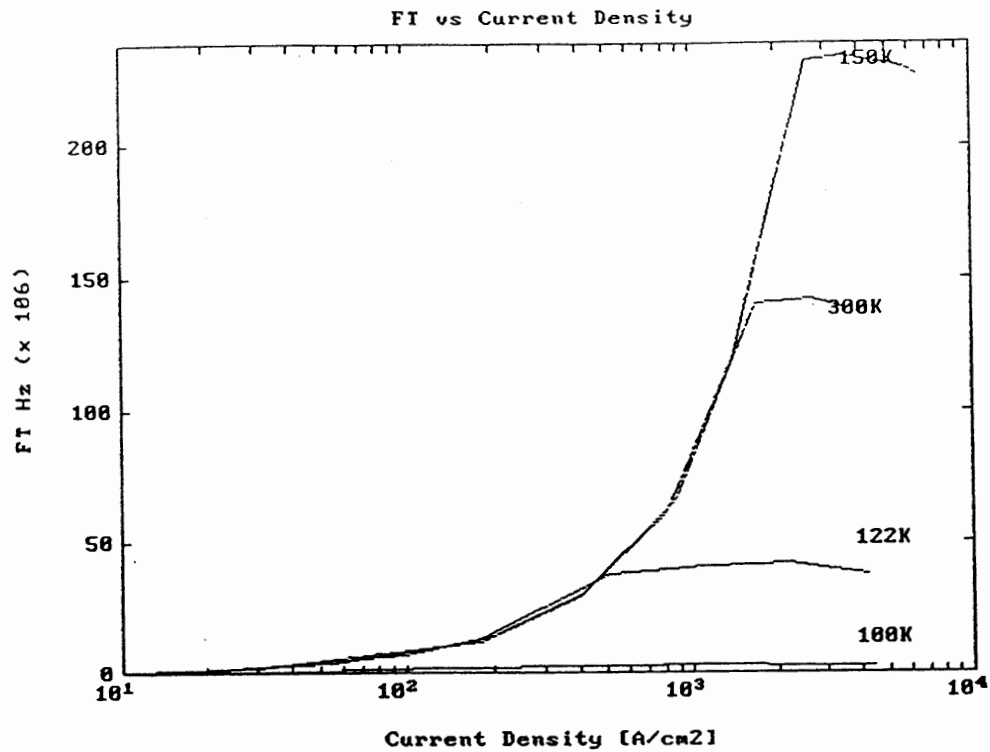


Figure 31 Unity gain frequency as a function of collector current at different temperatures for T1.

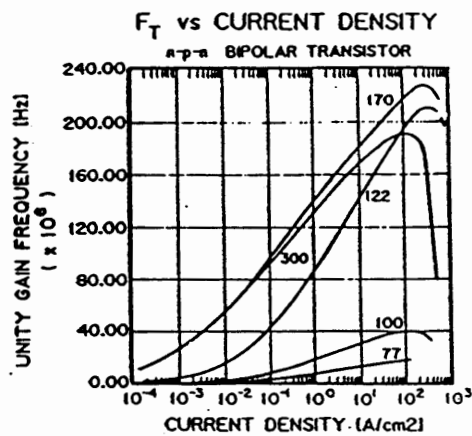


Figure 32 Unity gain frequency as a function of collector current at different temperatures from [5].

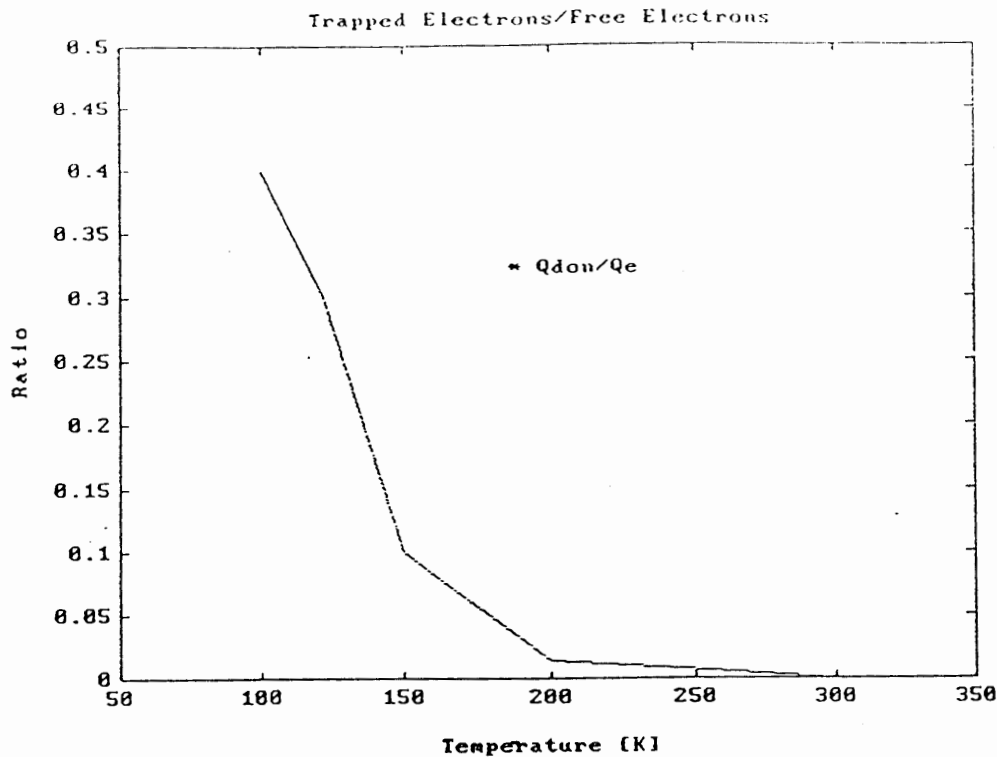


Figure 33 Temperature dependence of the ratio of the charge trapped on the donors in the base to the charge of free electrons in the base at peak f_T for T1

The degradation of the bipolar transistor performance at low temperature is caused not so much by the difference in the total charge stored in the transistor, as by the temperature dependence between the major components of the charge. This dependence should cause the current gain β to increase significantly as a function of collector current density. Unfortunately, the charge of the holes back injected into the emitter becomes dominant charge in the transistor at low temperature for high level injection conditions. This increase nullifies the increase in free electron charge in the base and causes the current gain to decrease. We present in Fig 34 the current gain versus current density and in Fig 35 the current gain versus current density obtained from the old BiLow [5]. Comparing Fig 34 with Fig 35, we see that there is a large current gain for temperature as low as

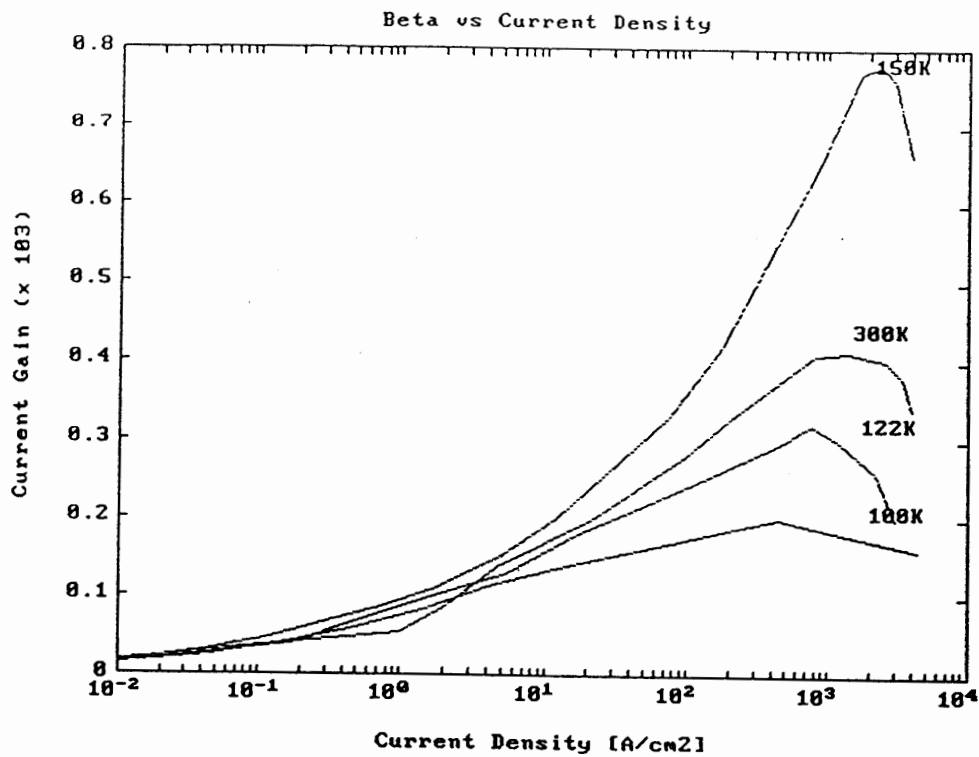


Figure 34 Current-gain as a function of collector current at different temperatures for T1.

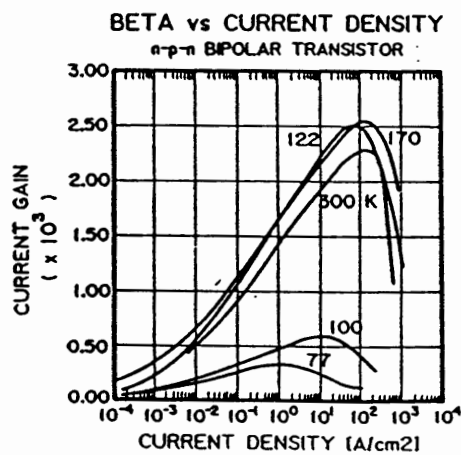


Figure 35 Current-gain as a function of collector current at different temperatures from [5]

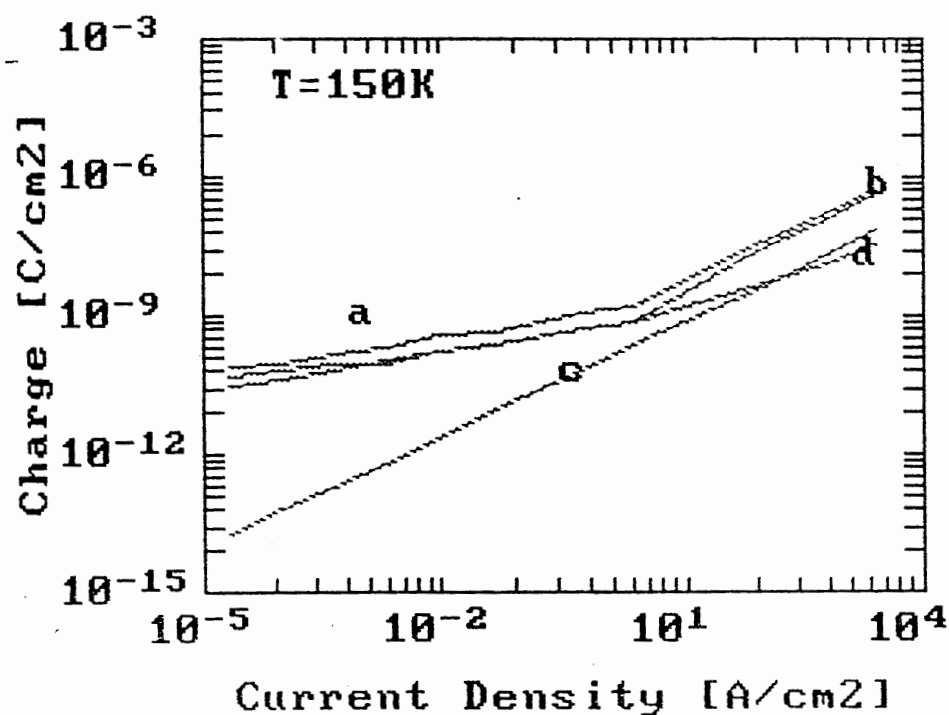
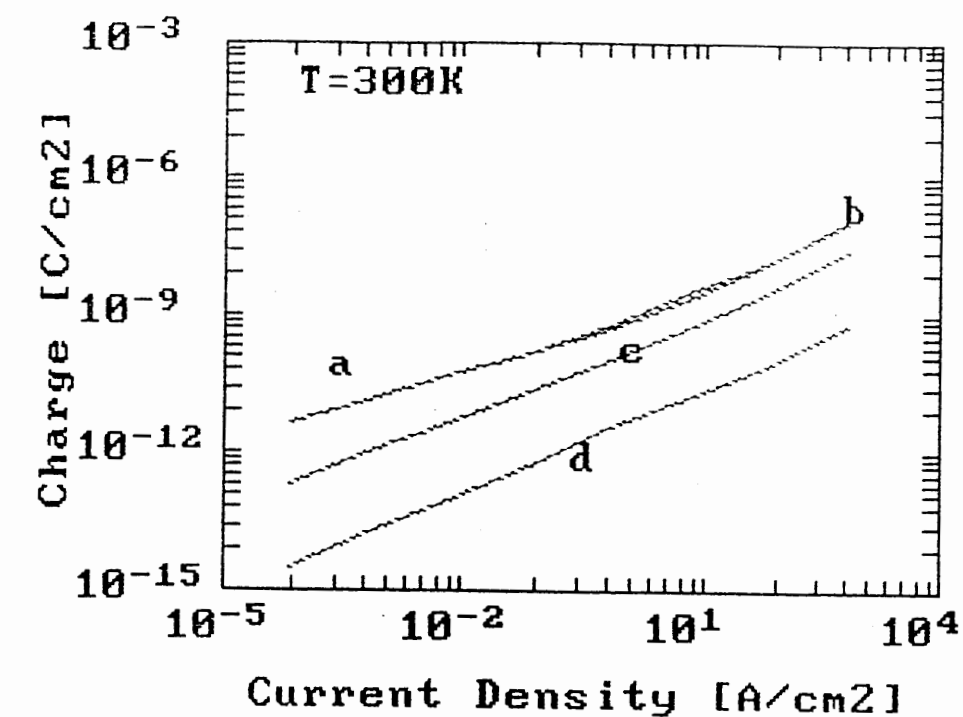


Figure 36. Charge Characteristics at (a) T=300K and (b) T=150K a = total charge
 b = charge of electrons in base, c = charge of holes in emitter, d = donors trapped in
 base using Method I.

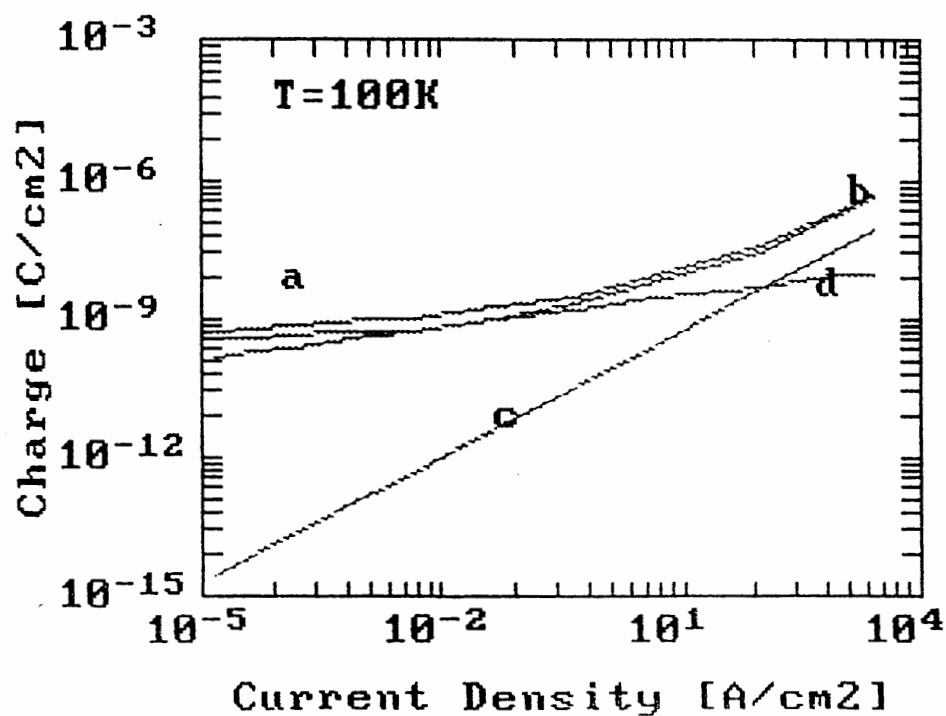
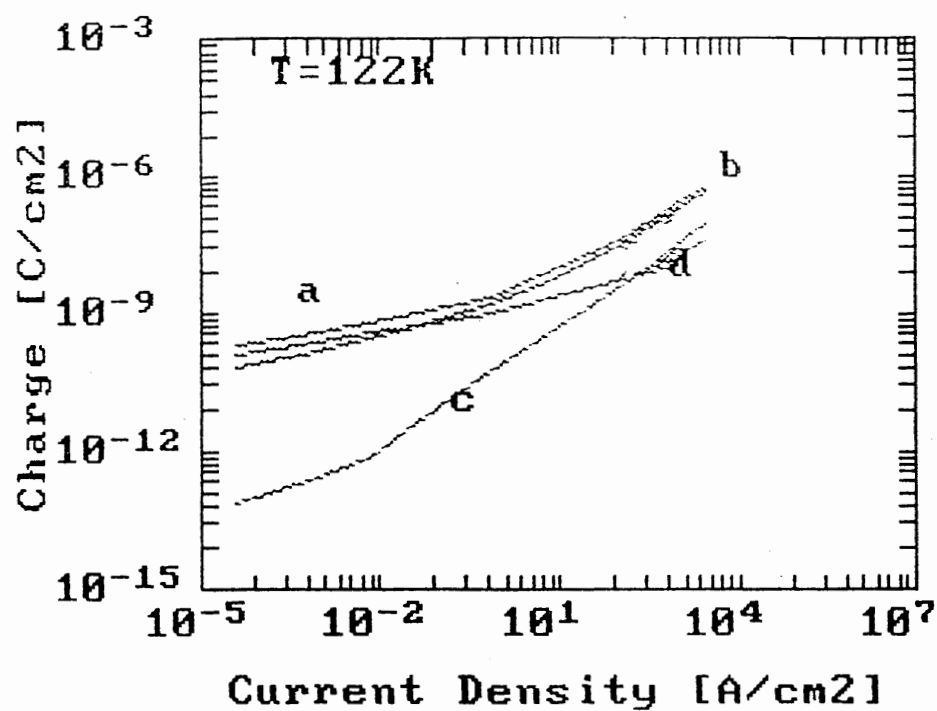


Figure 37. Charge Characteristics at (a) T=122K and (b) T=100K. a = total charge
 b = charge of electrons in base, c = charge of holes in emitter, d = donors trapped in
 base using Method I.

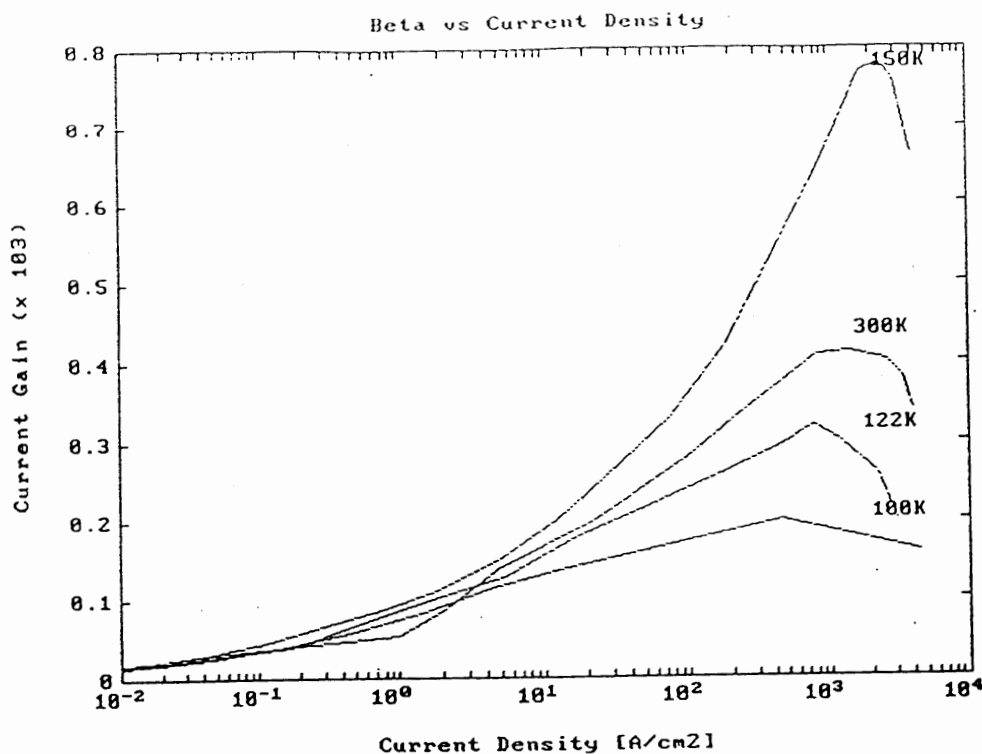


Figure 38. Temperature dependence of β using BGN Method I

122K obtained from the simulation results using the old BiLow. Also, the current gain at room temperature is smaller than the current gain at $T=122\text{K}$ in the old profile of Fig 25 whereas the current gain at room temperature is smaller than the β at $T=150\text{K}$ but higher than the β at $T=122\text{K}$ using T1. We conclude that this is mainly due to the higher base doping of transistor T1 which results in a larger base width at room temperature.

Simulations were also performed using the bandgap narrowing models, Method I and Method III, as described in Chapter IV. It was found that for the doping profile shown in Fig 18, the results of the simulations were identical, irrespective of the bandgap model used. The charge versus current density plots using bandgap model I are shown in Fig 36(a)-(b) and Fig 37(a)-(b). The plots of β and f_T as a function of current density for bandgap Method I are shown in Fig 38 and Fig 39 respectively. The plots of charge, β and f_T as a function of current density using bandgap Method III are shown in Fig 40(a)-

(b), Fig 41(a)-(b), Fig 42 and Fig 43 respectively. One can conclude that the models that were used may have been very close to show any mark difference in the simulation to

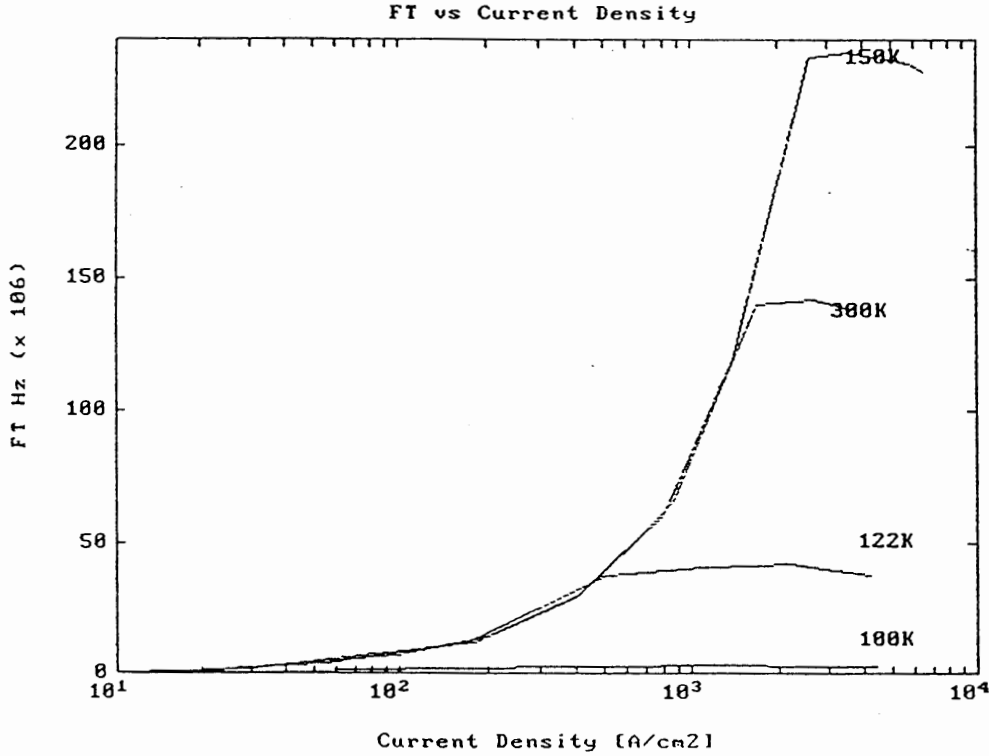


Figure 39. Temperature dependence of f_T using BGN Method I

model the BGN effect.

The doping profile of the second transistor is shown in Fig 44. In this profile we have taken into consideration the contribution from the epitaxial layer.

$$N_{epi} = N_b \exp\left(-\left(\frac{x_{jbc}}{x_b}\right)^2\right) \quad (100)$$

The simulations for transistor T2 were performed using bandgap model Method II. The temperature dependence of f_T is shown in Fig 45. As in Fig 29, the peak f_T is a function of temperature with a maximum around $T = 150\text{K}$. However, the corresponding values of

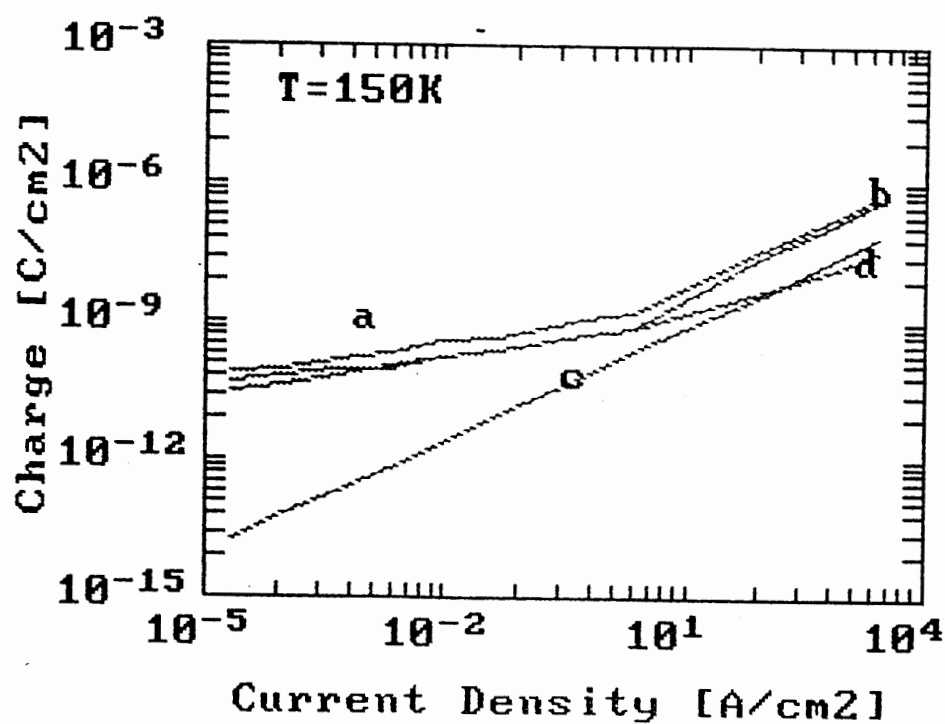
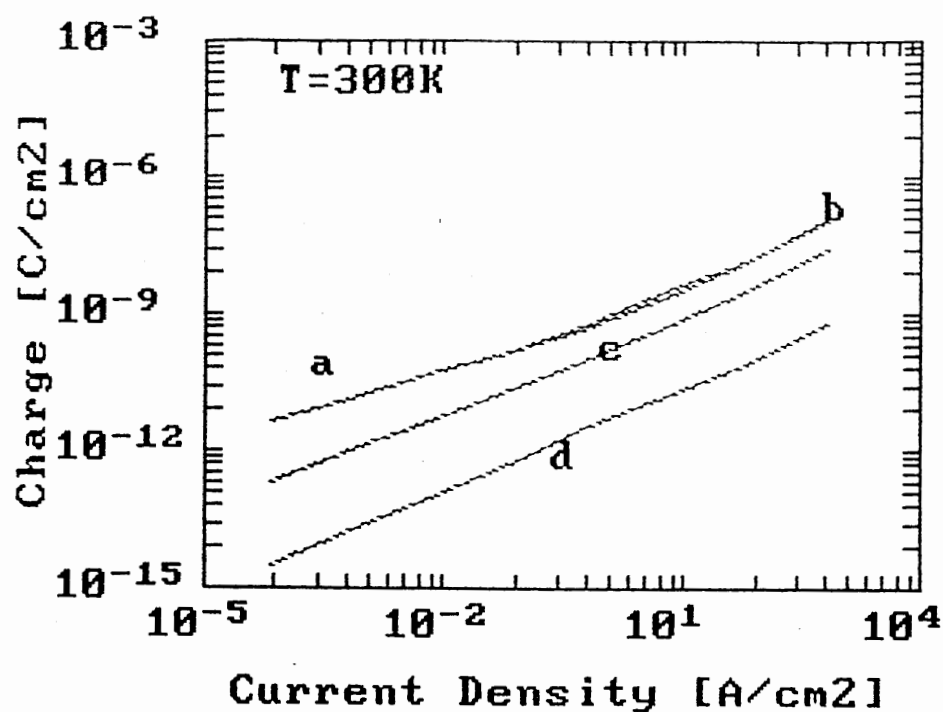


Figure 40. Charge Characteristics at (a) $T=300K$, (b) $T=150K$ a = total charge, b = charge of electrons in base, c = charge of holes in emitter, d = donors trapped in base, Using Method III.

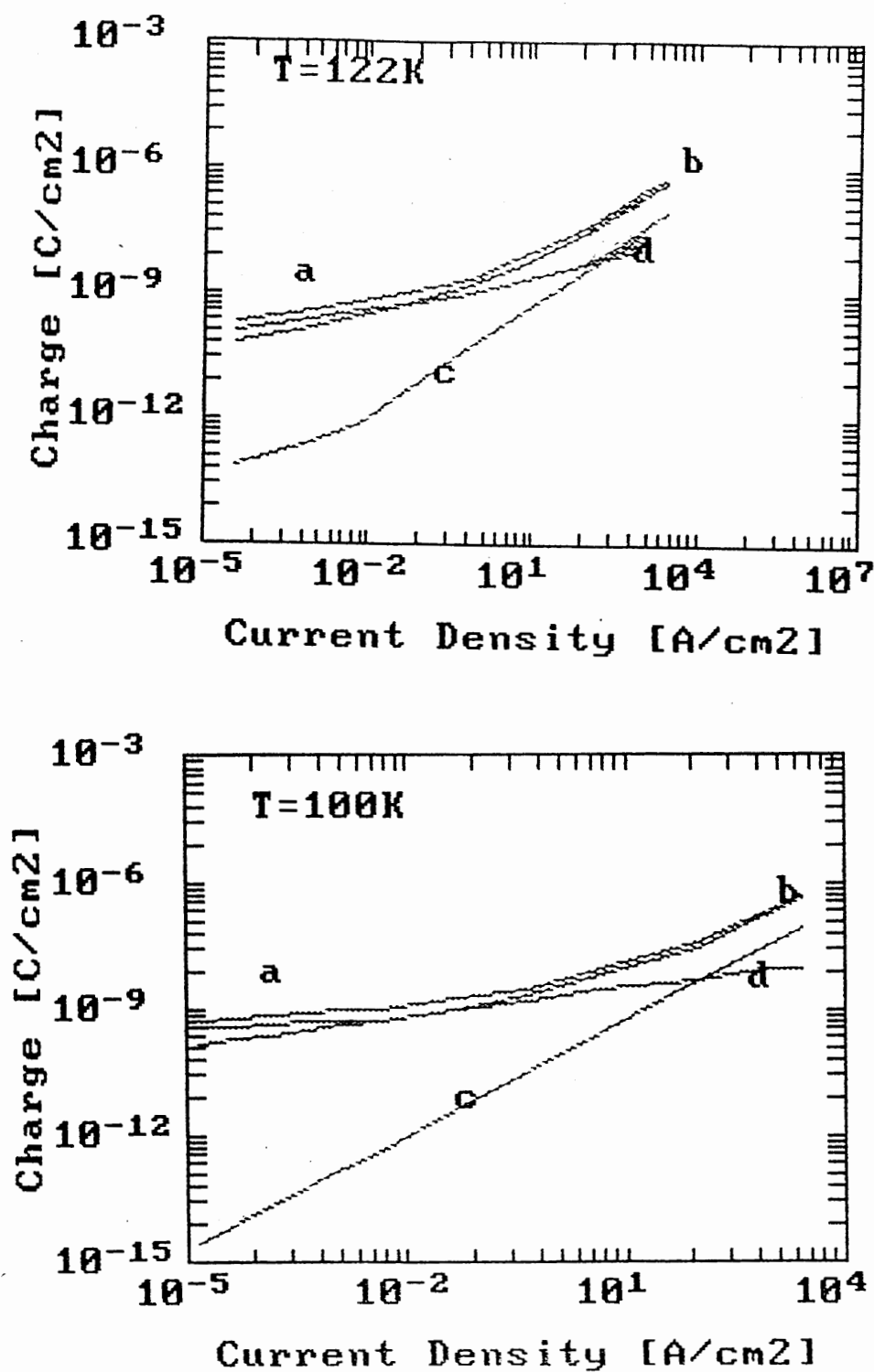


Figure 41. Charge Characteristics at (a) $T=122K$, (b) $T=100K$ a= total charge, b = charge of electrons in base, c = charge of holes in emitter, d = donors trapped in base, Using Method III.

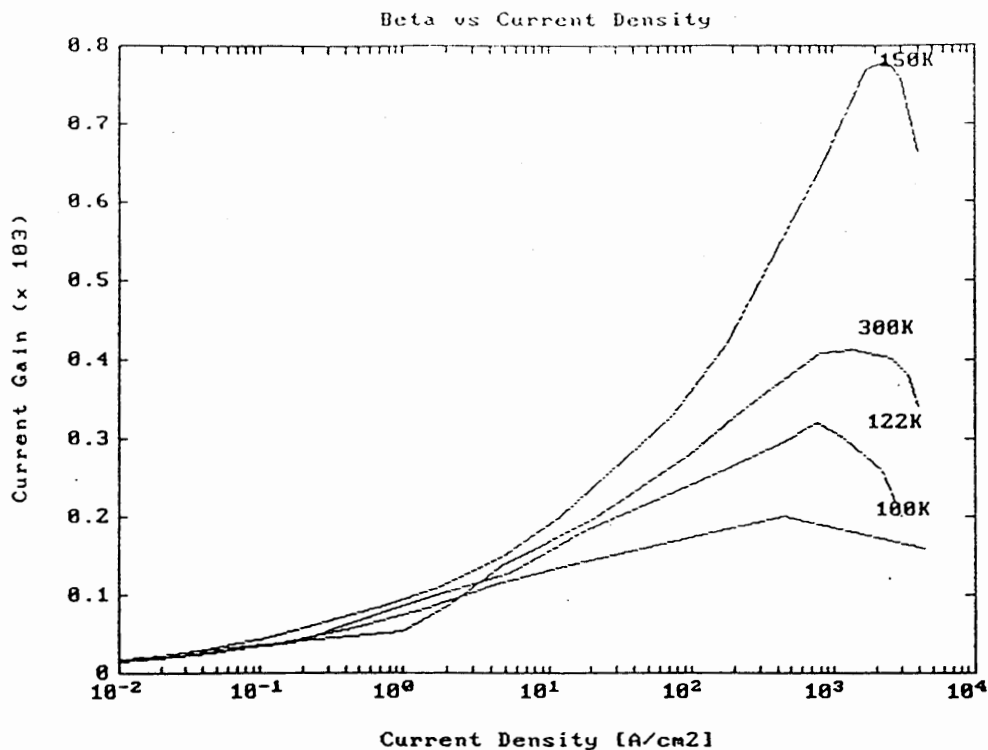


Figure 42. Temperature dependence of β using BGN Method III

f_T are higher than those observed in Fig 29. To reduce the base transient time, we need to minimize the collector resistance R_C . This can be done by using an epitaxial layer doped to withstand breakdown voltage, to be used as a collector. By considering the epitaxial layer in transistor T2, we have reduced the collector resistance thereby increasing the f_T and this is evident when we compare the temperature dependence of f_T from the profiles of transistors T1 and T2 as shown in Fig 29 and Fig 45. Fig 46(a)-(b) show the simulated charge characteristics as a function of current density at $T=300K$ and $T=77K$ for the transistor T2. For $T=77K$, the electrons trapped on the donor sites contribute mainly to the total charge for low and medium current densities. However, for high current densities, the charge of the holes back injected into the emitter dominates. Comparing the charge characteristics at $T=300K$ of Fig 19(a) with the charge of the new transistor T2 in

Fig 46(a), we see that the addition of the epilayer has not changed the total charge at room temperature. However, the total charge in the base of transistor T1 and T2 at room temperature, is larger at all values of collector current than that in transistor profile shown in Fig 23 and used in the original BiLow [6]. At $T=77\text{K}$, the total charge is mainly due to the charge of the holes back injected into the emitter and there is more charge at 77K in T2 than in the old profile. This can be clearly seen on comparing Fig 46(b) with Fig.26. The main reason for the difference in the amount of charge stored in the transistor is due to higher doping and smaller size of transistor T1 and T2 than the transistor profile shown in Fig.23.

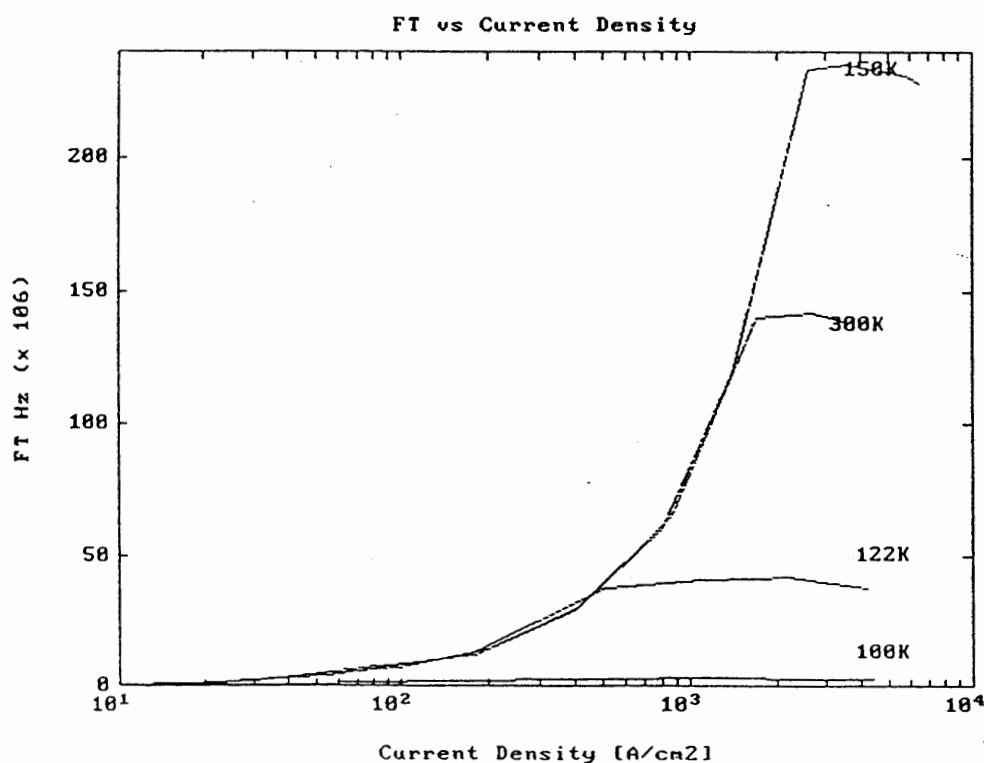


Figure 43. Temperature dependence of f_T using BGN Method III

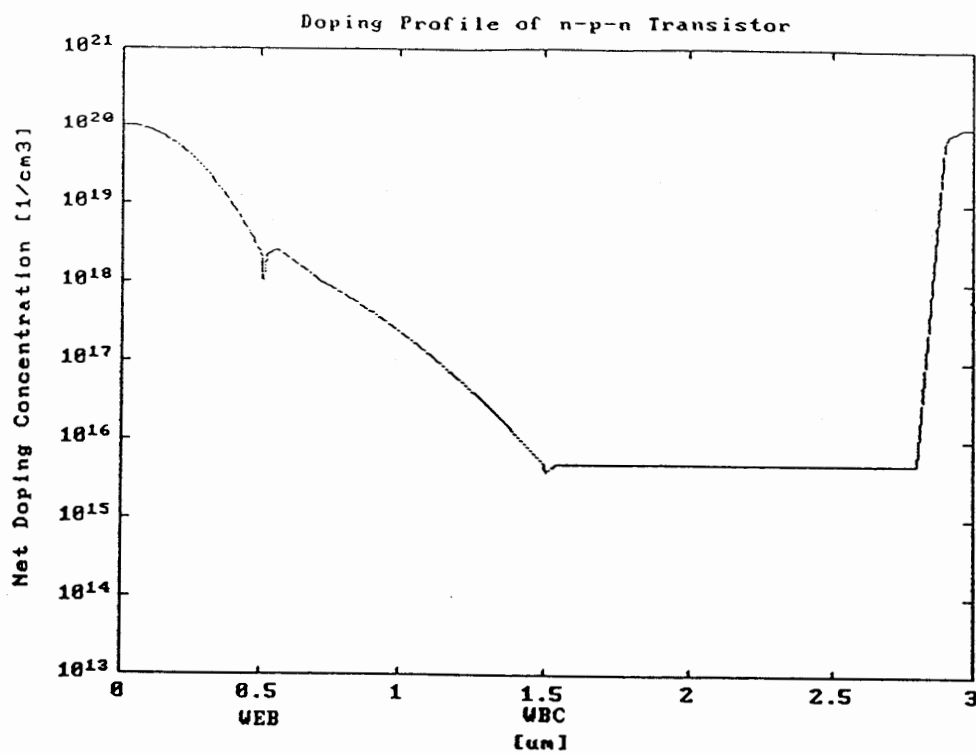


Figure 44. Doping profile of second transistor T2

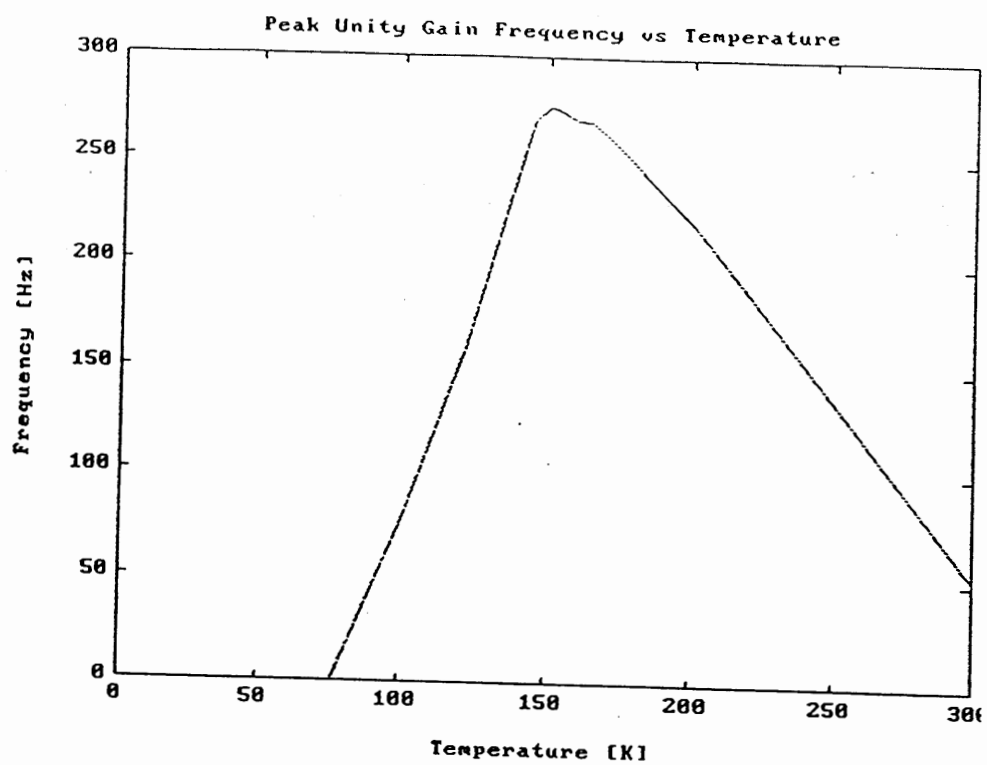


Figure 45. Temperature dependence of f_T for T2

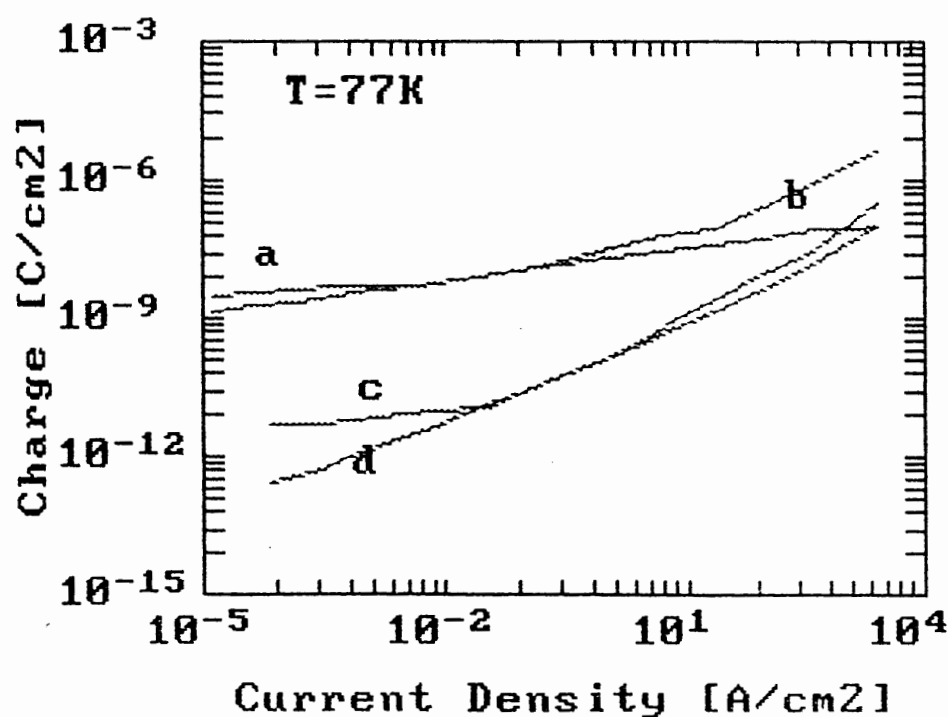
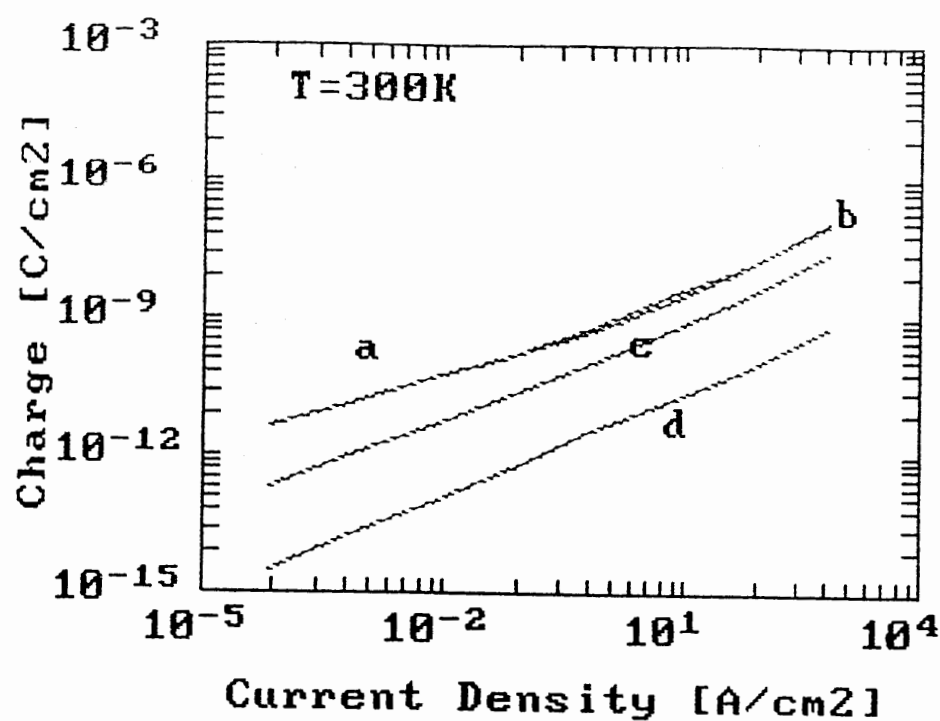


Figure 46. Charge Characteristics at (a) $T=122\text{K}$ and (b) $T=100\text{K}$. a = total charge, b = charge of electrons in base, c = charge of holes in emitter, d = charge of donors trapped in base for transistor T2.

CONCLUSIONS

A one-dimensional Bipolar transistor simulator has been enhanced for the purpose of proper memory management and easy modification. A model for incomplete ionization and Mott transition originally presented by Kuzmich [25] has been evaluated at low-temperature and then included in the simulator. An extensive literature survey on BGN, which is another important parameter for BJT performance at low-temperature, was carried out with the purpose of identifying models with performances in agreement with the experimental data. Two BGN models [27], [10] were selected to be used by the simulator along with the existing model which was modified by adding a Fermi-Dirac correction. The performance of the models was checked by low-temperature simulations of two double diffused n-p-n transistors. Ionization of impurity atoms has also been modeled in the simulator by adding Kuzmich incomplete ionization model [25].

Simulations have been done using all three of the BGN models. Two n-p-n transistor profiles were simulated with the new BiLow. It was found that the results were identical. This may be due to the fact that the three BGN models used may have been too similar to show any change in simulation results. Also the doping profile in the base could be selected appropriately so as to model the BGN effect. The doping profile that we used had a high concentration in the base. Since the BGN models considered for high doping concentration were almost identical, the simulation results were not affected by these different models. After analyzing our BGN results, we prefer to use BGN Method II [27] for our simulations as it is valid for moderate and heavy doping. However, the simulator can select any of the three BGN models at a time. The effect of Mott transition on the abrupt decrease in the electron concentration in emitter has been taken care of by

smoothing out the concentration profile in the emitter thereby providing a continuity in the region of Mott transition. Both the current gain and the frequency values obtained from simulating the two new profiles were found to be smaller than those from the original BiLow [5] as the doping in the base is higher and the device sizes were smaller. Most of the degradation in β and f_T was found to occur below 150K. From the charge characteristics plots we find that the total charge which is a strong function of temperature is higher for the profiles studied with the new BiLow than that obtained from the profiles used in the original BiLow simulator[5].

Future application of the BiLow program includes enhancing the current convergence criteria by including the net recombination/generation of carriers at low-temperature. Improving other existing parameters in the simulator, including the heat transfer equations and extending the simulator to two-dimensions.

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